PRODUCTS DATA SHEET



MATSUO ELECTRIC CO., LTD.

OUTLINE

The signal transmission rate of personal computer peripheral devices and digital devices as represented by USB2.0 devices is being increased year by year, and countermeasures against ESD are critical in high-frequency bands. We have developed Type KVA Surge Absorber to protect the circuits of various electronic devices sensitive to ESD.

Since the surge absorber has a low capacitance of 0.08 pF, it is applicable to high-speed signal lines.

The ecology design of Type KVA is environmentally friendly because of Lead-free and Halogen-free.

APPLICATION

The product is suitable for elimination of ESD on high-speed signal lines that may be affected by signal waveform deformation. (USB2.0, IEEE1394, HDMI interfaces, SCSI ports, etc.)

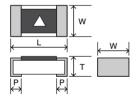
FEATURES

- 1. Usable on high-speed signal lines
- 2. Low capacitance (Representative value: 0.08 pF)
- 3. Large ESD endurance and high insulation resistance
- 4. No polarity. Protection of circuit against ESD from both directions
- 5. Ultra-small size, 1608 (1.6 \times 0.8 \times 0.45 mm)
- 6. Suitable for automatic mounting by chip placer
- 7. Precise dimensions allows high-density mounting and symmetrical construction of terminal provide "Self-Alignment".
- 8. Resistance to soldering heat: Reflow or flow soldering 10 seconds at 260°C
- 9. High accuracy carrier tape by using pressed pocket ensures excellent mounting.
- 10. Lead-free and RoHS Compliant

RATING

Item	Ratings	
Category Temperature Range	-40 ~ +125°C	
Rated Voltage	24 VDC	
Trigger Voltage	1000 V max. (650 V typ.)	
Clamp Voltage	200 V max. (100 V typ.)	
Capacitance	0.2 pF max. (0.08 pF typ.)	
Leakage Current	1 nA max. (measuring voltage: 6 V)	

DIMENSIONS



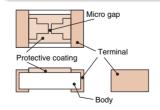
Main body : Alumina ceramic Terminals : Tin plating

Case size	Case code	L	W	T max.	Р
1608	29	1.60 ^{±0.1}	0.80 ^{±0.1}	0.45	0.30 ^{±0.2}
					(mm)

MARKING

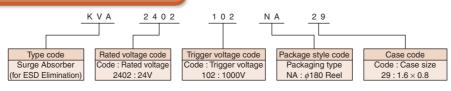
Code	Rated voltage	Trigger voltage
Δ	24 VDC	1000 V max.

CONSTRUCTION



Name	Material
Micro gap	Copper
Body	Alumina ceramic
Protective coat	Silicone resin
Terminal	Tin plating

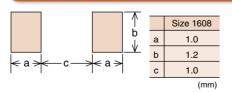
ORDERING INFORMATION



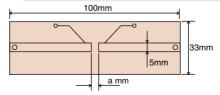
PERFORMANCE

No.	Item	Performance	Test method
			Contact discharging conforming to IEC61000-4-2
1	Trigger voltage	Shall not exceed 1000 V.	Tester capacity : 150 pF/Resistance: 330 Ω
			Contact discharging conforming to IEC61000-4-2
2	Clamp voltage	Shall not exceed 200 V.	Tester capacity : 150 pF/Resistance: 330 Ω
			Test voltage : 8 kV (level 4)
3	Capacitance	Shall not exceed 0.2 pF.	Measuring frequency: 1 MHz
4	Leakage current	Shall not exceed 1 nA.	Measuring voltage : 6 V
5	Insulation resistance	Shall not exceed 1000 MΩ.	Resistance between terminals and case (alumina ceramic)
			Board supporting width: 90 mm
6	Electrode strength	No mechanical damage.	Bending speed : Approx. 0.5 mm/sec
0	(Flexibility)	Shall meet the trigger voltage.	Duration : 30 sec
			Bending : 3 mm
			Applied force : 20 N (2.04 kgf)
7	Shear test	No mechanical damage.	Duration: 10 sec
	onour tool	Shall meet the trigger voltage.	Tool: R0.5
			Direction of the press : side face
			Supporting dimension : 0.8 mm
8	Substrate bending test	No mechanical damage.	Applied force: 10 N (1.02 kgf)
ŭ	Cabonato bonanig toot	Shall meet the trigger voltage.	Tool: R0.5
			Direction of the press : thickness direction of product.
			Dipping: 3 sec
			Temperature : 245 ± 5°C
		The dipped surface of the terminals	Solder: Sn-3Ag-0.5Cu alloy
9	Solderability	shall be covered more than 95% with new solder.	Dipping: 3 sec
			Temperature : 235 ± 5°C
			Solder: Conforms to JIS Z 3282 (solder)
			H60A, H60S, H63A
			Dipping (1 cycle)
			Preconditioning: 100 ~ 150°C, 60 sec
			Temperature : 265 ± 3°C/6 ~ 7 sec
			Reflow soldering (2 cycles)
		Marking shall be legible.	Preconditioning: 1 ~ 2 min, 180°C or less
10	Resistance to soldering heat	No mechanical damage.	Peak : 250 ± 5°C, 5 sec
.0	Tresistance to soldering near	Shall meet the trigger voltage.	Holding : 230 ~ 250°C, 30 ~ 40 sec
		onal most the trigger voltage.	Cooling : more than 2 min
			Manual soldering
			Temperature : 350 ± 10°C
			Duration: 3 ~ 4 sec
			Measure after 1 hour left under room temp. and humidity.
		Marking shall be legible.	Dipping rinse
11	Solvent resistance	No mechanical damage.	Solvent : Isopropyl alcohol
		Shall meat the trigger voltage.	Duration : 90 sec
		No mechanical damage.	Frequency range : 10 ~ 55 ~ 10 Hz/min
12	Vibration	Shall meet the trigger voltage.	Vibration amplitude : 1.5 mm
		35 0	Duration : 2 hours in each of XYZ directions (total : 6 hours)
40	Oh I	No mechanical damage.	Peak value: 490 m/s² (50 G)
13	Shock	Shall meet the trigger voltage.	Duration: 11 ms
		-	6 aspects × 3 times (total : 18 times)
			-55 ± 3°C : 30 min
4.4	The amount of a set	No mechanical damage.	Room temperature : 2 ~ 3 min or less
14	Thermal shock	Shall meet the trigger voltage.	125 ± 2°C : 30 min
			Room temperature : 2 ~ 3 min or less
			Repeat above step for 10 cycles.
		No weathering demand	Temperature: 85 ± 3°C
15	Moisture resistance	No mechanical damage.	Humidity: 85 ± 5% RH
		Shall meet the trigger voltage.	Leaving
			Duration : 1000 hours
16	Load life	No mechanical damage.	Temperature: 85 ± 2°C
10	Load life	Shall meet the trigger voltage.	Applied current : 24 V (rated voltage)
			Duration : 1000 hours
17	Chability	No mechanical damage.	Temperature : 125 ± 2°C
17	Stability	Shall meet the trigger voltage.	Leaving
			Duration : 1000 hours
		No mark ariant damage	Contact discharging conforming to IEC61000-4-2
		INO mechanical damage.	
18	ESD endurance	No mechanical damage. The resistance between terminals shall be 1 M Ω or more, and the trigger voltage	Tester capacity : 150 pF/Resistance : 330 Ω
18	ESD endurance	No mechanical damage. The resistance between terminals shall be 1 M Ω or more, and the trigger voltage shall be met.	

RECOMMENDED PAD DIMENSIONS



STANDARD TEST BOARD

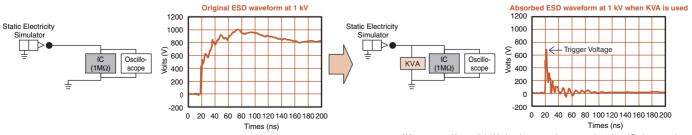


Glass epoxy on one side Board thickness : 1.6 mm Copper layer : 35 µm

Case	Size
size	а
1608	1.2
	(mm)

STATIC SUPPRESSION - Example of ESD Elimination -

Surge Absorber absorbs and suppresses static electricity.



▲ Application Notes for Surge Absorber

1. Circuit Design

Type KVA Surge Absorber is a part for protection from static electricity and cannot be used for protection from lightning surge. Before using Type KVA Surge Absorber, sufficiently examine its electrical characteristics and the circuit conditions to be mounted.

- Type KVA should always be operated below the rated voltage.
- (2) Use Type KVA under the condition of category temperature.

Type KVA should be selected by determining the operating conditions that will occur after final assembly, or estimating potential abnormalities through cycle testing.

2. Assembly and Mounting

During the entire assembly process, observe Type KVA body temperature and the heating time specified in the performance table. In addition, observe the following items:

- Mounting and adjusting with soldering irons are not recommendable since temperature and time control is difficult.
 - In case of emergency for using soldering irons, be sure to observe the conditions specified in the performance table.
- (2) Type KVA body should not have direct contact with a soldering iron.
- (3) Once Type KVA mounted on the board, they should never be remounted on boards or substrates.
- (4) During mounting, be careful not to apply any excessive mechanical stresses to Type KVA.

3. Solvents

For cleaning of Type KVA, immersion in isopropyl alcohol for 90 seconds (at 20 ~ 30°C liquid temp.) will not be damaged. If organic solvents (Pine AlphaTM, Techno CareTM, Clean ThroughTM, etc.) will be applied to Type KVA, be sure to preliminarily check that the solvent will not damage the Type KVA

4. Caution During Usage

Type KVA should never be touched in use.

5. Environmental Conditions

- (1) Type KVA should not be operated in acid, alkali, or active gas atmosphere.
- (2) Type KVA should not be vibrated, shocked, or pressed excessively.
- (3) Type KVA should not be operated in a flammable or explosive atmosphere.
- (4) After mounting Type KVA on a board, covering Fuses with resin may affect to the electric characteristics of Type KVA. Please be sure to evaluate it in advance.

6. Emergency

In case of fire, smoking, or offensive odor during operation, please cut off the power in the circuit or pull the plug out.

7. Storage

- (1) Type KVA should be stored at room temperature (-10°C ~ +40°C) without direct sunlight. Direct sunlight may cause decolorization and deformation of the exterior and taping. Also, there is a fear that solderability will be remarkably lower in high humidity.
- (2) If the products are stored for an extended period of time, please contact Matsuo Sales Department for recommendation. The longer storage term causes packages and tapings to worsen. If the products are stored for longer term, please contact Matsuo Sales Department for advice.
- (3) The products in taping, package, or box should not be given any kind of physical pressure. Deformation of taping or package may affect automatic mounting.

8. Disposa

When Type KVA are disposed of as waste or "scrap", they should be treated as "industrial waste". Type KVA contain various kinds of metals and resins.

9. Samples

Type KVA received as samples should not be used in any products or devices in the market. Samples are provided for a particular purpose such as configuration, confirmation of electrical characteristics, etc.

MATSUO ELECTRIC CO., LTD.

Please feel free to ask our sales department for more information on the Surge Absorber.

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The specifications on this catalog are subject to change without prior notice. Please inquire of our Sales Department to confirm the specifications prior to use.