

General Description

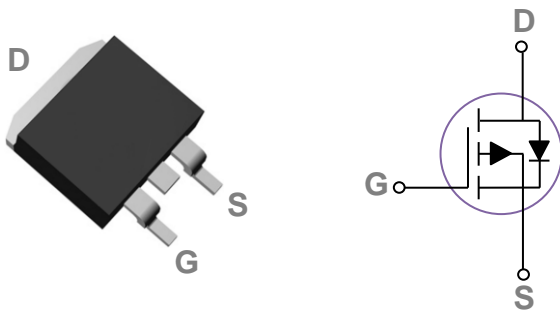
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-30V	3.8mΩ	-165A

Features

- -30V,-165A, RDS(ON) 3.8mΩ @VGS = -10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

TO263 Pin Configuration



Applications

- MB / VGA / Server Vcore
- POL Applications
- SMPS 2nd SR
- BMS System

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (T _C =25°C)	-165	A
	Drain Current – Continuous (T _C =100°C)	-106	A
I _{DM}	Drain Current – Pulsed ¹	-660	A
EAS	Single Pulse Avalanche Energy ²	231	mJ
IAS	Single Pulse Avalanche Current ²	-68	A
P _D	Power Dissipation (T _C =25°C)	222	W
	Power Dissipation – Derate above 25°C	1.78	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	0.56	°C/W

Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-30V, V_{GS}=0V, T_J=25^\circ C$	---	---	-1	μA
		$V_{DS}=-24V, V_{GS}=0V, T_J=125^\circ C$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-30A$	---	3.2	3.8	m Ω
		$V_{GS}=-4.5V, I_D=-20A$	---	4.2	5.5	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.2	-1.6	-2.5	V
gfs	Forward Transconductance	$V_{DS}=-10V, I_S=-3A$	---	20	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2,3}	$V_{DS}=-15V, V_{GS}=-10V, I_D=-80A$	---	150	250	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	24	40	
Q_{gd}	Gate-Drain Charge ^{2,3}		---	28	45	
$T_{d(on)}$	Turn-On Delay Time ^{2,3}	$V_{DD}=-15V, V_{GS}=-10V, R_G=6\Omega, I_D=-80A$	---	25	40	ns
T_r	Rise Time ^{2,3}		---	35	55	
$T_{d(off)}$	Turn-Off Delay Time ^{2,3}		---	100	150	
T_f	Fall Time ^{2,3}		---	50	80	
C_{iss}	Input Capacitance	$V_{DS}=-15V, V_{GS}=0V, F=1MHz$	---	7500	12000	pF
C_{oss}	Output Capacitance		---	1200	1800	
C_{rss}	Reverse Transfer Capacitance		---	940	1400	
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	---	3.5	---	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V, \text{Force Current}$	---	---	-165	A
I_{SM}	Pulsed Source Current		---	---	-330	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=-1A, T_J=25^\circ C$	---	---	-1	V
t_{rr}	Reverse Recovery Time	$V_R=-30V, I_S=-10A$	---	210	---	ns
Q_{rr}	Reverse Recovery Charge	$di/dt=100A/\mu s, T_J=25^\circ C$	---	580	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-68A, \text{Starting } T_J=25^\circ C$
3. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

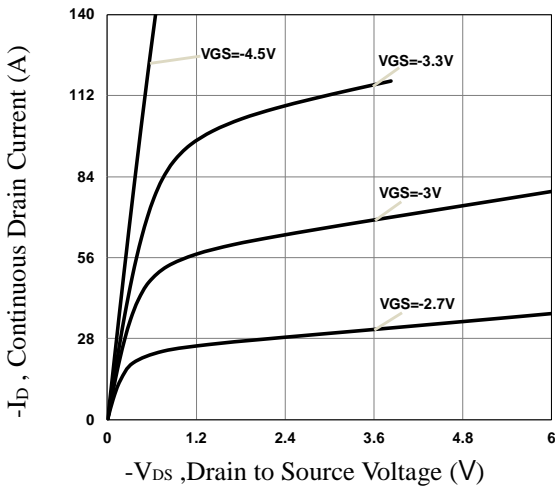


Fig.1 Typical Output Characteristics

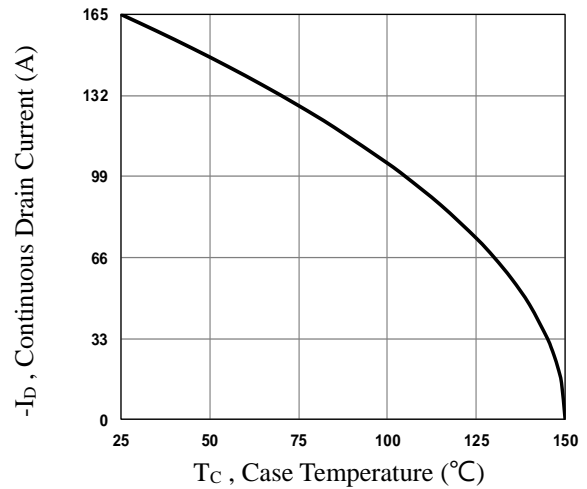


Fig.2 Continuous Drain Current vs. T_c

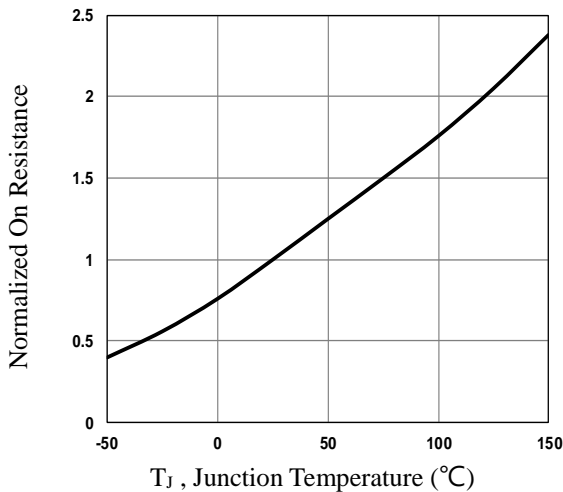


Fig.3 Normalized R_{DS(on)} vs. T_j

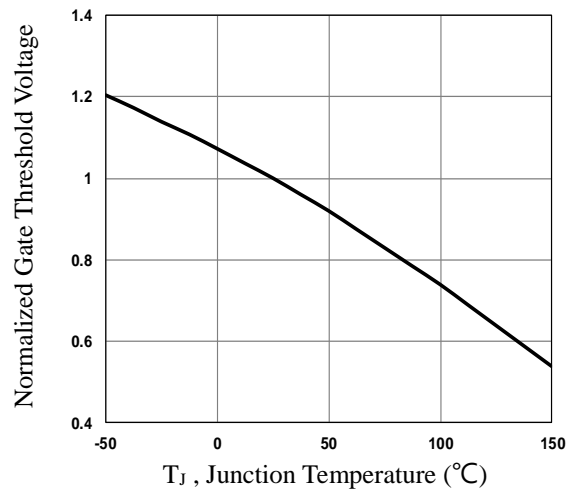


Fig.4 Normalized V_{th} vs. T_j

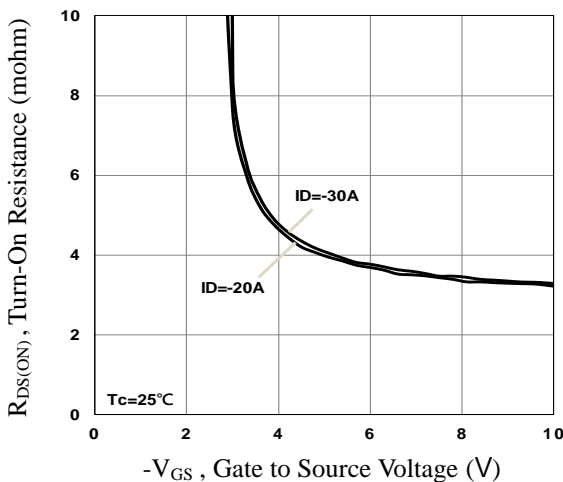


Fig.5 Turn-On Resistance vs. V_{GS}

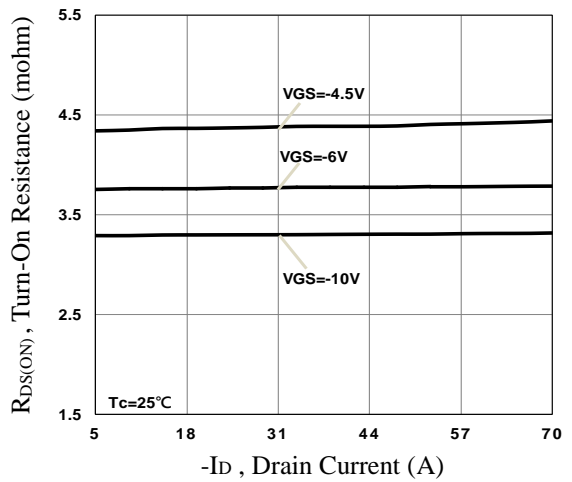


Fig.6 Turn-On Resistance vs. I_D

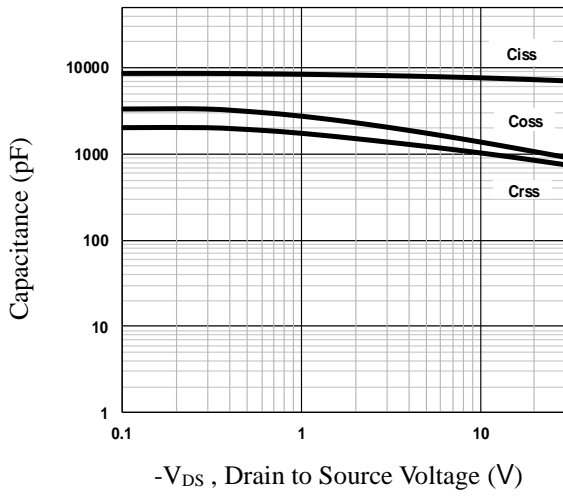


Fig.7 Capacitance Characteristics

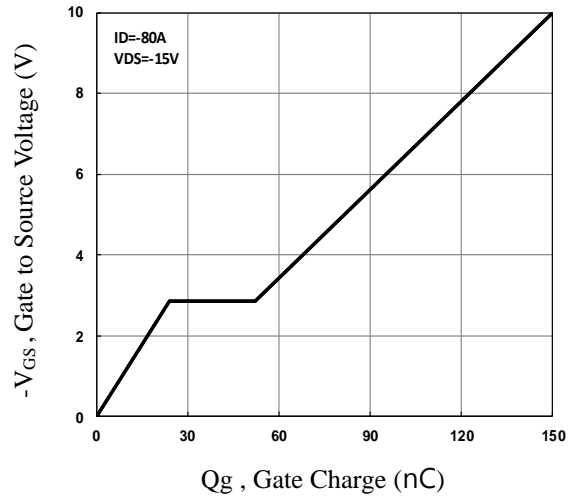


Fig.8 Gate Charge Characteristics

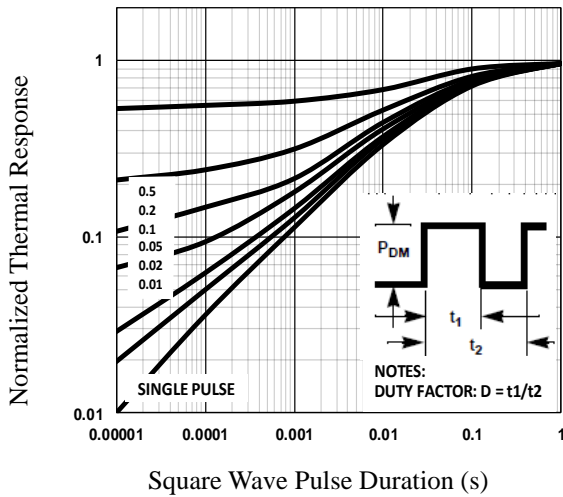


Fig.9 Normalized Transient Impedance

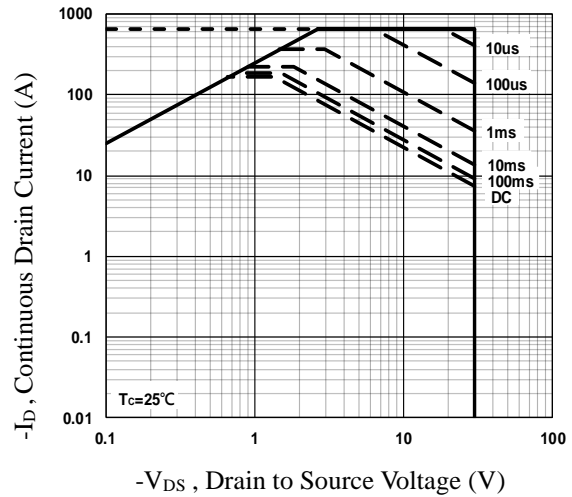


Fig.10 Maximum Safe Operation Area



Fig.11 Switching Time Waveform

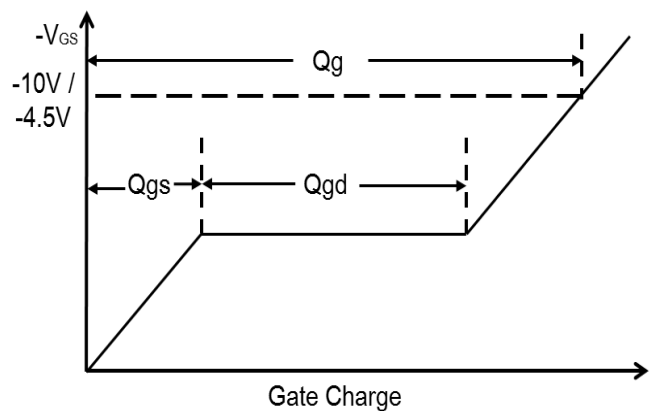
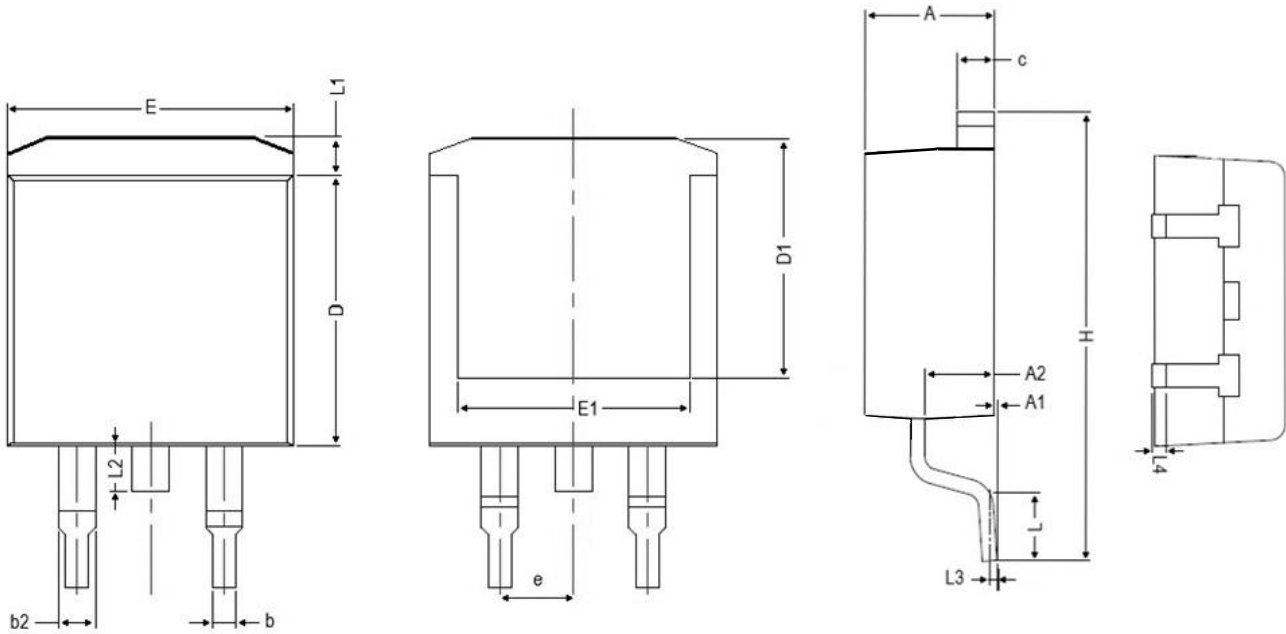


Fig.12 Gate Charge Waveform

TO263 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	4.850	4.250	0.191	0.167
A1	0.250	0.000	0.001	0.000
A2	2.900	2.350	0.114	0.093
b	0.950	0.700	0.037	0.028
b2	1.600	1.000	0.063	0.039
c	1.450	1.200	0.057	0.047
D	9.500	8.350	0.374	0.329
D1	9.150	6.400	0.360	0.252
E	10.500	9.600	0.413	0.378
E1	8.900	7.500	0.350	0.295
e	2.540 BSC		0.100 BSC	
H	15.900	14.600	0.626	0.575
L	2.800	2.000	0.110	0.079
L1	1.700	1.150	0.067	0.045
L2	2.100	1.400	0.083	0.055
L3	0.250 BSC		0.010 BSC	
L4	0.750	0.200	0.030	0.001