

General Description

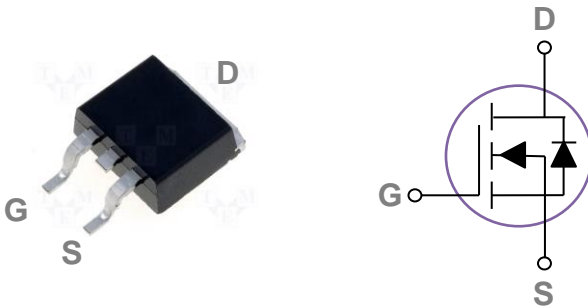
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
80V	9.1mΩ	80A

Features

- 80V,80A, $R_{DS(ON)} = 9.1m\Omega @ V_{GS} = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

TO263 Pin Configuration



Applications

- Networking
- Load Switch
- LED applications

Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	80	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ C$)	80	A
	Drain Current – Continuous ($T_c=100^\circ C$)	51	A
I_{DM}	Drain Current – Pulsed ¹	320	A
EAS	Single Pulse Avalanche Energy ²	230	mJ
IAS	Single Pulse Avalanche Current ²	68	A
P_D	Power Dissipation ($T_c=25^\circ C$)	184	W
	Power Dissipation – Derate above $25^\circ C$	1.47	W/ $^\circ C$
T_{STG}	Storage Temperature Range	-50 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-50 to 150	$^\circ C$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.68	$^\circ C/W$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	80	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V, T _J =25°C	---	---	1	μA
		V _{DS} =64V, V _{GS} =0V, T _J =125°C	---	---	10	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =15A	---	7.6	9.1	mΩ
		V _{GS} =4.5V, I _D =8A	---	8.8	11	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250μA	1.2	1.8	3	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =3A	---	16	---	S

Dynamic and switching Characteristics

Q _g	Total Gate Charge ^{3,4}	V _{DS} =40V, V _{GS} =10V, I _D =10A	---	86	170	nC
Q _{gs}	Gate-Source Charge ^{3,4}		---	14.5	28	
Q _{gd}	Gate-Drain Charge ^{3,4}		---	17.5	35	
T _{d(on)}	Turn-On Delay Time ^{3,4}	V _{DD} =40V, V _{GS} =10V, R _G =10Ω I _D =10A	---	21.8	42	ns
T _r	Rise Time ^{3,4}		---	27.8	54	
T _{d(off)}	Turn-Off Delay Time ^{3,4}		---	111	220	
T _f	Fall Time ^{3,4}		---	56	112	
C _{iss}	Input Capacitance	V _{DS} =40V, V _{GS} =0V, F=1MHz	---	6810	12000	pF
C _{oss}	Output Capacitance		---	292	580	
C _{rss}	Reverse Transfer Capacitance		---	188	390	
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	---	1.6	3.2	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	80	A
I _{SM}	Pulsed Source Current		---	---	160	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1	V
t _{rr}	Reverse Recovery Time	V _{GS} =0V, I _S =10A, di/dt=100A/μs	---	39	---	ns
Q _{rr}	Reverse Recovery Charge	T _J =25°C	---	47	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V_{DD}=50V, V_{GS}=10V, L=0.1mH, I_{AS}=68A., Starting T_J=25°C
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

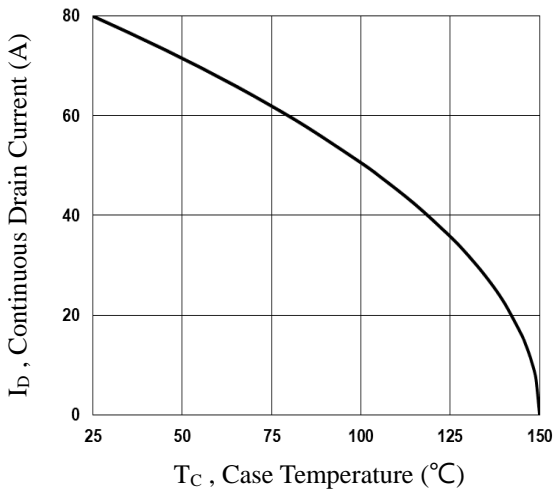


Fig.1 Continuous Drain Current vs. T_c

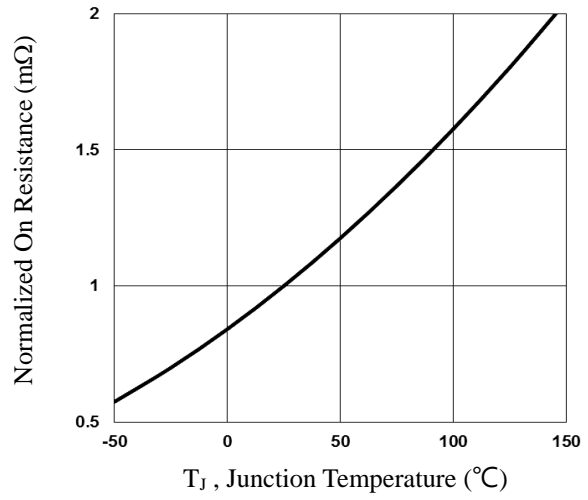


Fig.2 Normalized $R_{DS(on)}$ vs. T_j

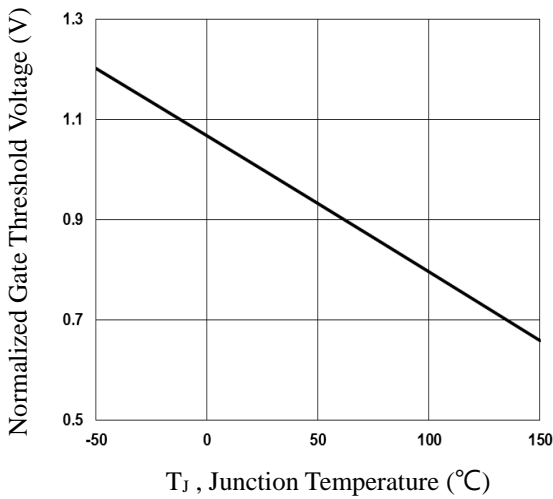


Fig.3 Normalized V_{th} vs. T_j

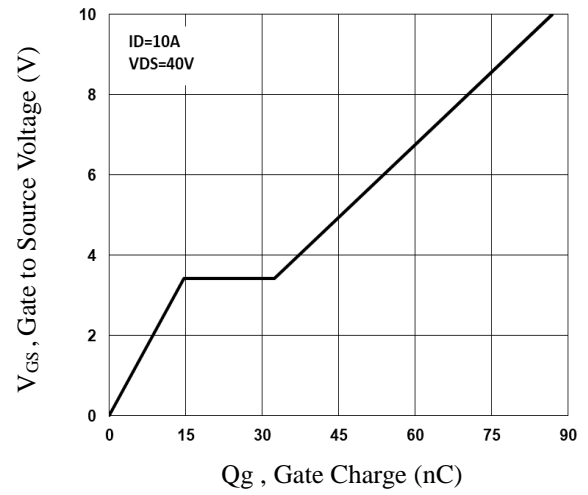


Fig.4 Gate Charge Characteristics

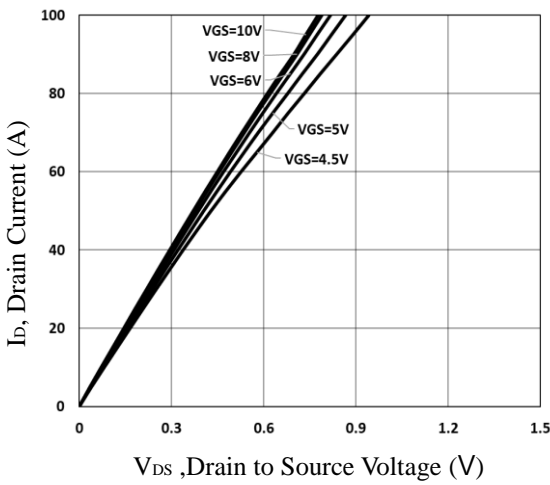


Fig.5 Typical Output Characteristics

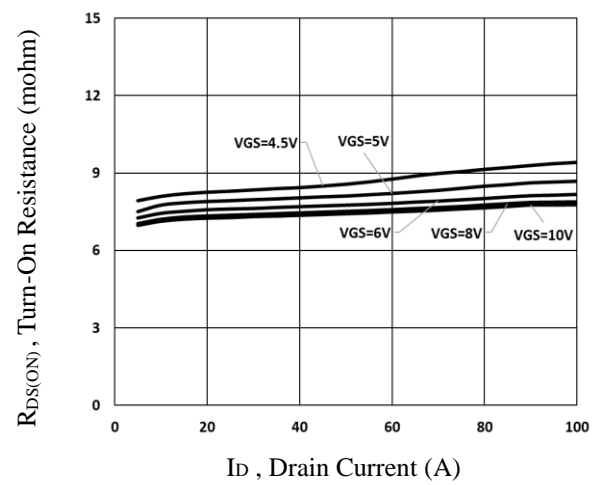


Fig.6 Turn-On Resistance vs. I_D

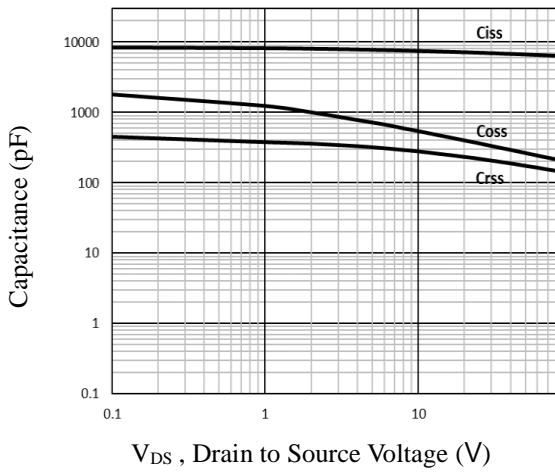


Fig.7 Capacitance Characteristics

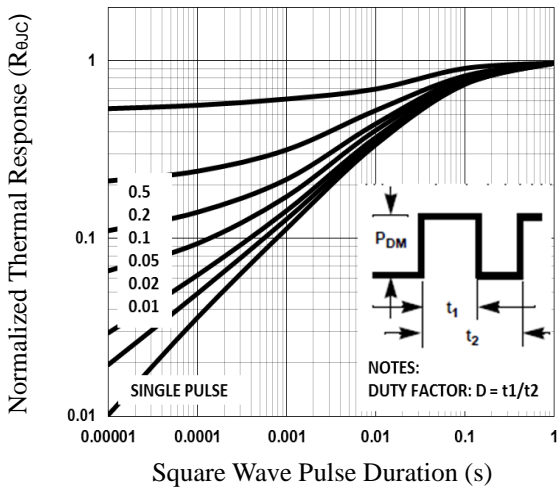


Fig.8 Normalized Transient Impedance

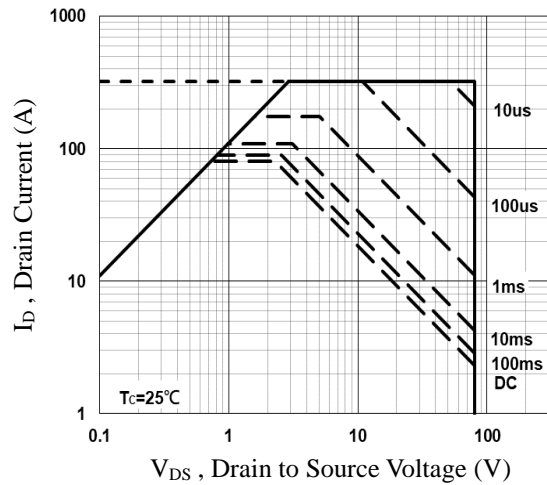


Fig.9 Maximum Safe Operation Area

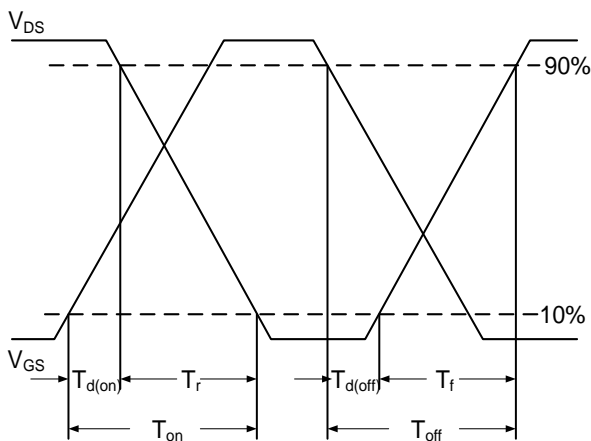


Fig.10 Switching Time Waveform

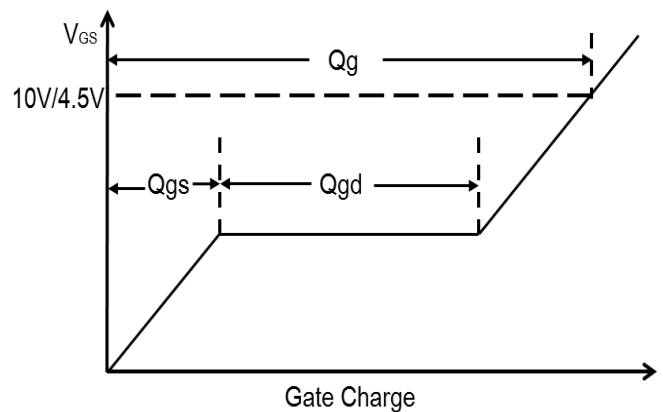


Fig.11 Gate Charge Waveform

TO263 PACKAGE INFORMATION

