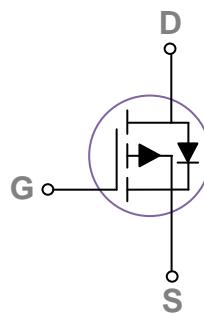
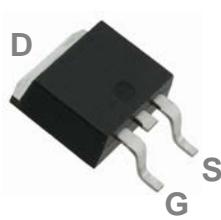


### General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### TO252 Pin Configuration



BVDSS	RDS(ON)	ID
-60V	180mΩ	-7A

### Features

- -60V, -7A, RDS(ON) = 180mΩ@VGS = -10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Motor Drive
- Power Tools
- LED Lighting

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	-7	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	-4.4	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	-28	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	32	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	-8	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	15.6	W
	Power Dissipation – Derate above 25°C	0.125	W/°C
$T_{STG}$	Storage Temperature Range	-50 to 150	°C
$T_J$	Operating Junction Temperature Range	-50 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	8	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$	-60	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=-1\text{mA}$	---	-0.05	---	$\text{V}/^\circ\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-60\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	-1	$\mu\text{A}$
		$V_{\text{DS}}=-48\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	-10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$ , $I_D=-3\text{A}$	---	153	180	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$ , $I_D=-1.5\text{A}$	---	198	220	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=-250\mu\text{A}$	-1.2	-1.6	-2.5	V
			---	5	---	$\text{mV}/^\circ\text{C}$
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$ , $I_D=-2\text{A}$	---	3	---	S

**Dynamic and switching Characteristics**

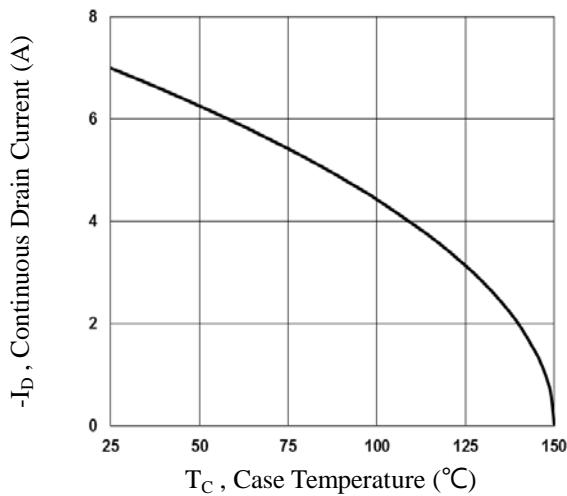
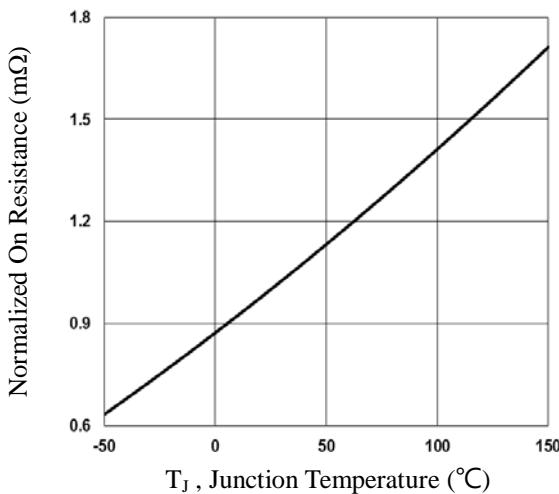
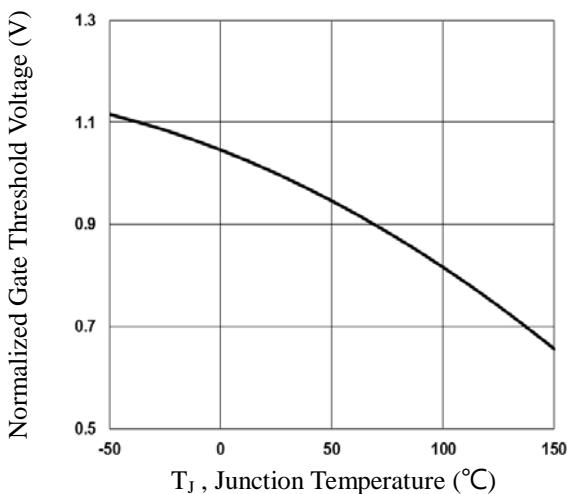
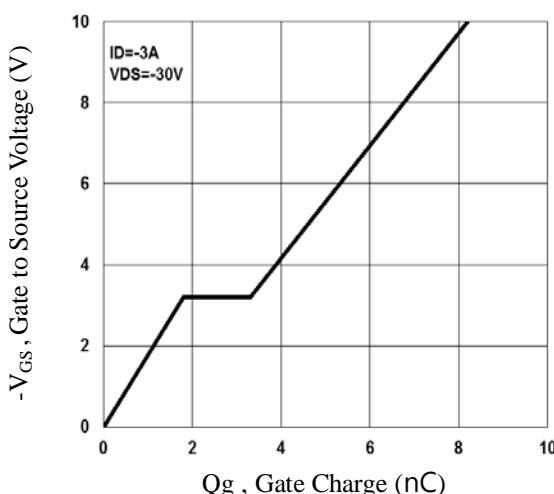
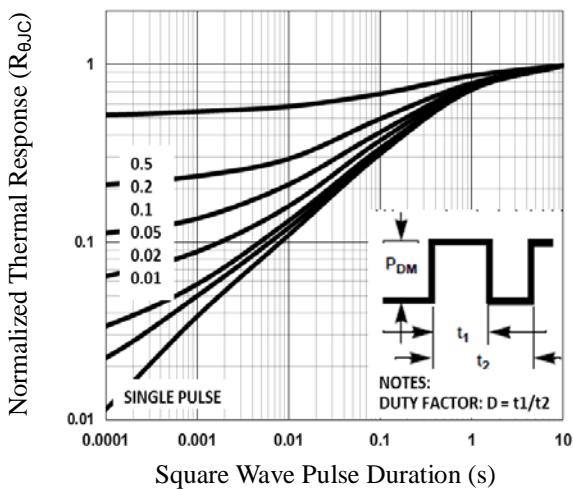
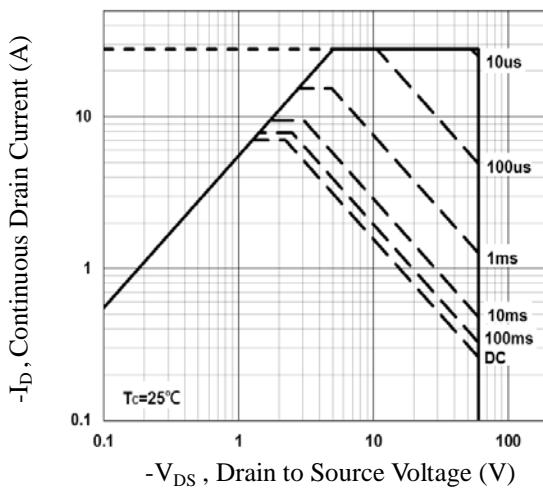
$Q_g$	Total Gate Charge <sup>3,4</sup>	$V_{\text{DS}}=-30\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $I_D=-3\text{A}$	---	8.2	12	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>3,4</sup>		---	1.8	3.6	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>3,4</sup>		---	1.5	3	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>3,4</sup>	$V_{\text{DD}}=-30\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $R_G=6\Omega$	---	5.2	10	ns
$T_r$	Rise Time <sup>3,4</sup>		---	19	36	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>3,4</sup>		---	35	67	
$T_f$	Fall Time <sup>3,4</sup>		---	10.6	20	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-30\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	425	615	pF
$C_{\text{oss}}$	Output Capacitance		---	35	50	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	20	30	

**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-7	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	-28	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=-1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{\text{DD}}=-25\text{V}$ ,  $V_{\text{GS}}=-10\text{V}$ ,  $L=1\text{mH}$ ,  $I_{\text{AS}}=-8\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\text{us}$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1** Continuous Drain Current vs.  $T_C$ 

**Fig.2** Normalized RDSON vs.  $T_J$ 

**Fig.3** Normalized  $V_{th}$  vs.  $T_J$ 

**Fig.4** Gate Charge Waveform

**Fig.5** Normalized Transient Impedance

**Fig.6** Maximum Safe Operation Area

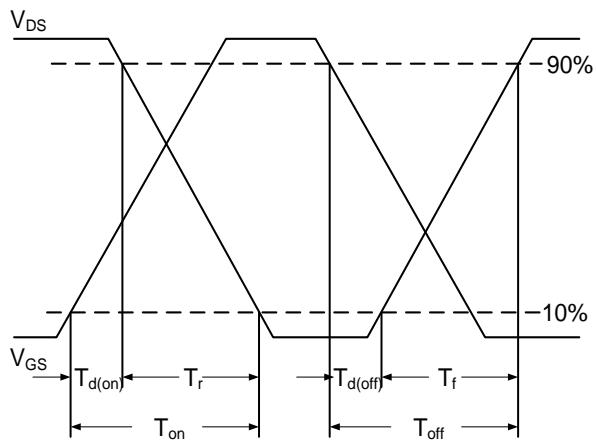


Fig.7 Switching Time Waveform

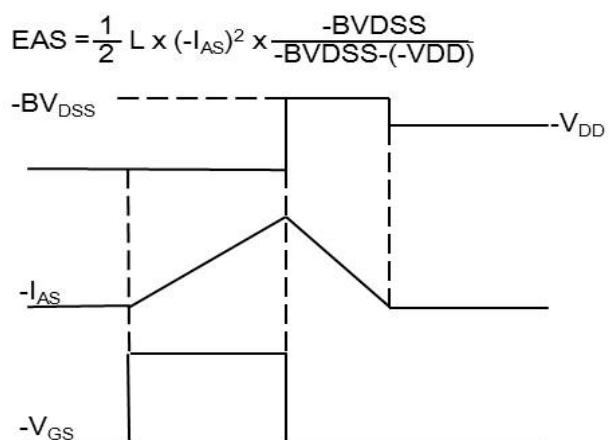
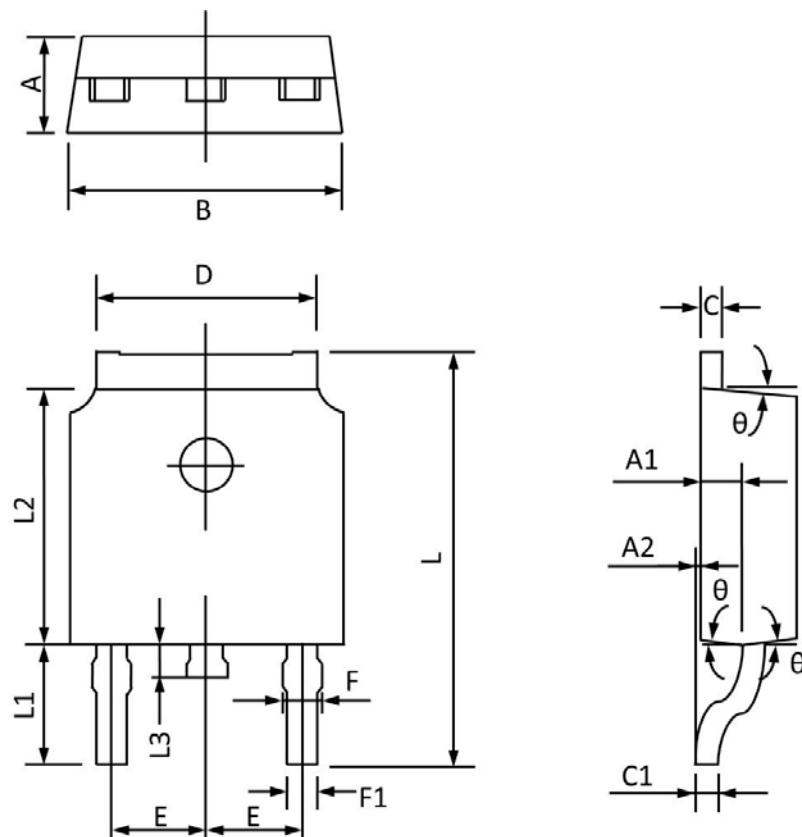


Fig.8 EAS Waveform

## TO252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.20	2.40	0.087	0.094
A1	0.91	1.11	0.036	0.044
A2	0.00	0.15	0.000	0.006
B	6.50	6.70	0.256	0.264
C	0.46	0.580	0.018	0.230
C1	0.46	0.580	0.018	0.030
D	5.10	5.46	0.201	0.215
E	2.186	2.386	0.086	0.094
F	0.74	0.94	0.029	0.037
F1	0.660	0.860	0.026	0.034
L	9.80	10.40	0.386	0.409
L1	2.9REF		0.114REF	
L2	6.00	6.20	0.236	0.244
L3	0.60	1.00	0.024	0.039
θ	3°	9°	3°	9°