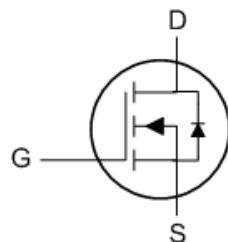
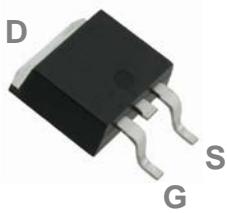


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### TO252 Pin Configuration



BVDSS	RDS(ON)	ID
30V	6mΩ	80A

### Features

- 30V,80A, RDS(ON) =6mΩ@VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- MB / VGA / Vcore
- POL Applications
- SMPS 2<sup>nd</sup> SR

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	80	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	51	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	320	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	88	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	42	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	54	W
	Power Dissipation – Derate above 25°C	0.43	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2.3	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Static State Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$	---	0.04	---	$\text{V}/^\circ\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=30\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=24\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{GS}=10\text{V}$ , $I_D=20\text{A}$	---	4.8	6	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=10\text{A}$	---	6.5	9	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D = 250\mu\text{A}$	1	1.6	2.5	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	-4	---	$\text{mV}/^\circ\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=10\text{A}$	---	18	---	S

**Dynamic Characteristics**

$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{DS}=15\text{V}$ , $V_{GS}=4.5\text{V}$ , $I_D=20\text{A}$	---	11.1	---	nC
$Q_{gs}$	Gate-Source Charge <sup>3, 4</sup>		---	1.85	---	
$Q_{gd}$	Gate-Drain Charge <sup>3, 4</sup>		---	6.8	---	
$T_{d(on)}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{DD}=15\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=3.3\Omega$ $I_D=15\text{A}$	---	7.5	---	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	14.5	---	
$T_{d(off)}$	Turn-Off Delay Time <sup>3, 4</sup>		---	35.2	---	
$T_f$	Fall Time <sup>3, 4</sup>		---	9.6	---	
$C_{iss}$	Input Capacitance	$V_{DS}=25\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	1160	---	pF
$C_{oss}$	Output Capacitance		---	200	---	
$C_{rss}$	Reverse Transfer Capacitance		---	180	---	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	2.5	---	$\Omega$

**Guaranteed Avalanche Energy**

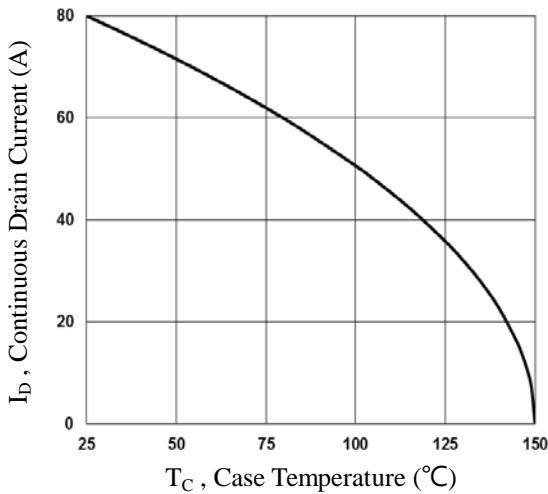
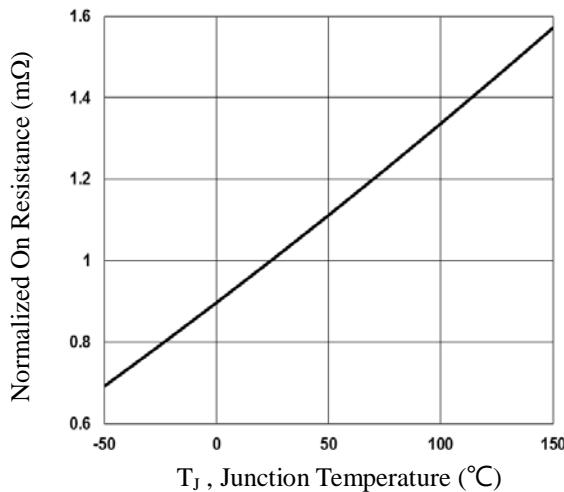
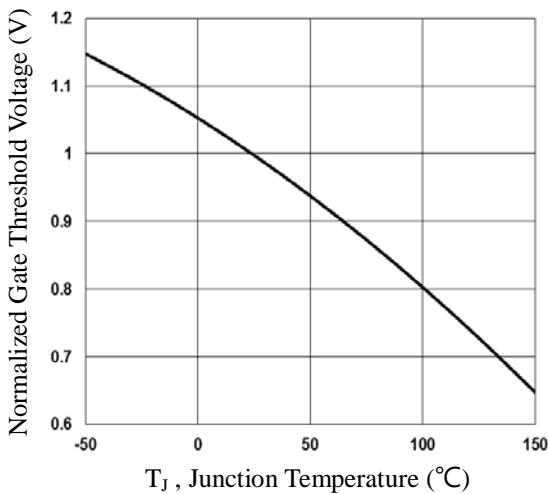
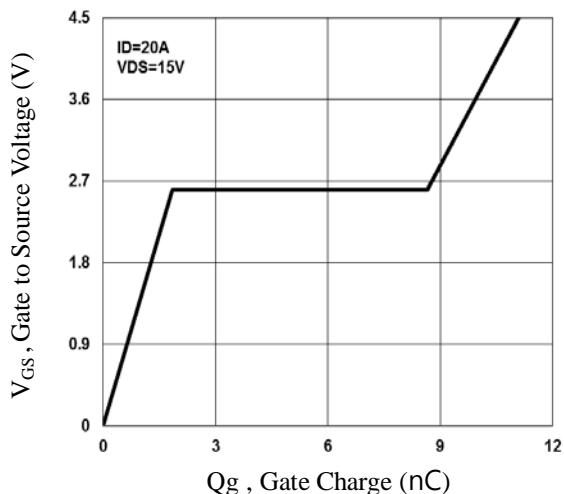
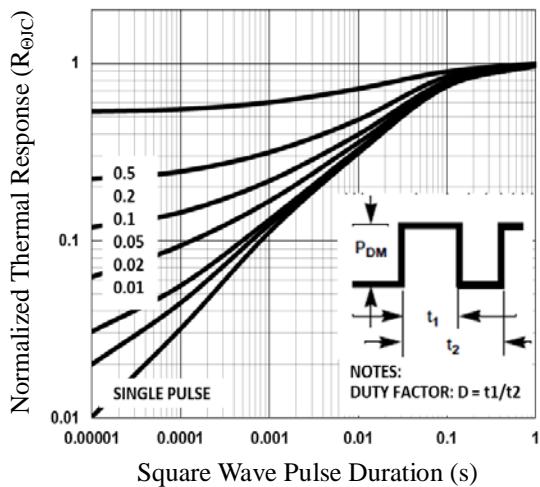
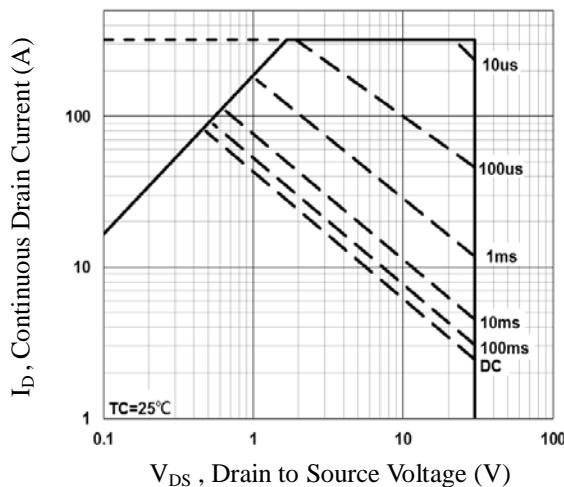
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy	$V_{DD}=25\text{V}$ , $L=0.1\text{mH}$ , $I_{AS}=20\text{A}$	20	---	---	mJ

**Drain-Source Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	80	A
$I_{SM}$	Pulsed Source Current <sup>3</sup>		---	---	320	A
$V_{SD}$	Diode Forward Voltage <sup>3</sup>	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V
$t_{rr}$	Reverse Recovery Time	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $di/dt=100\text{A}/\mu\text{s}$	---	---	---	ns
$Q_{rr}$	Reverse Recovery Charge	$T_J=25^\circ\text{C}$	---	---	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=42\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1** Continuous Drain Current vs.  $T_C$ 

**Fig.2** Normalized RDS(on) vs.  $T_J$ 

**Fig.3** Normalized  $V_{th}$  vs.  $T_J$ 

**Fig.4** Gate Charge Waveform

**Fig.5** Normalized Transient Impedance

**Fig.6** Maximum Safe Operation Area

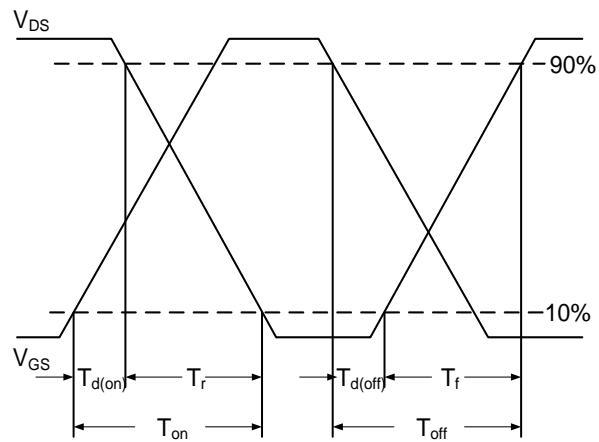


Fig.7 Switching Time Waveform

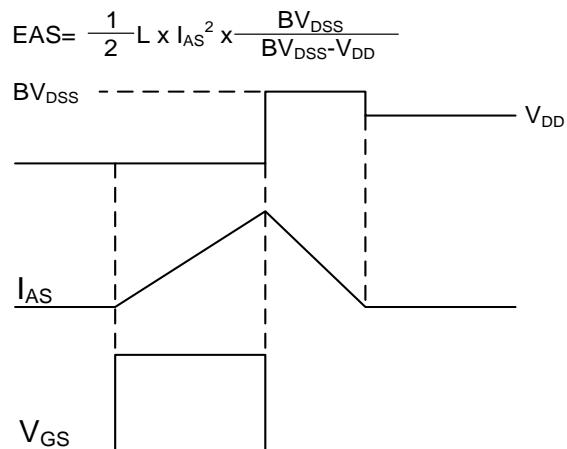
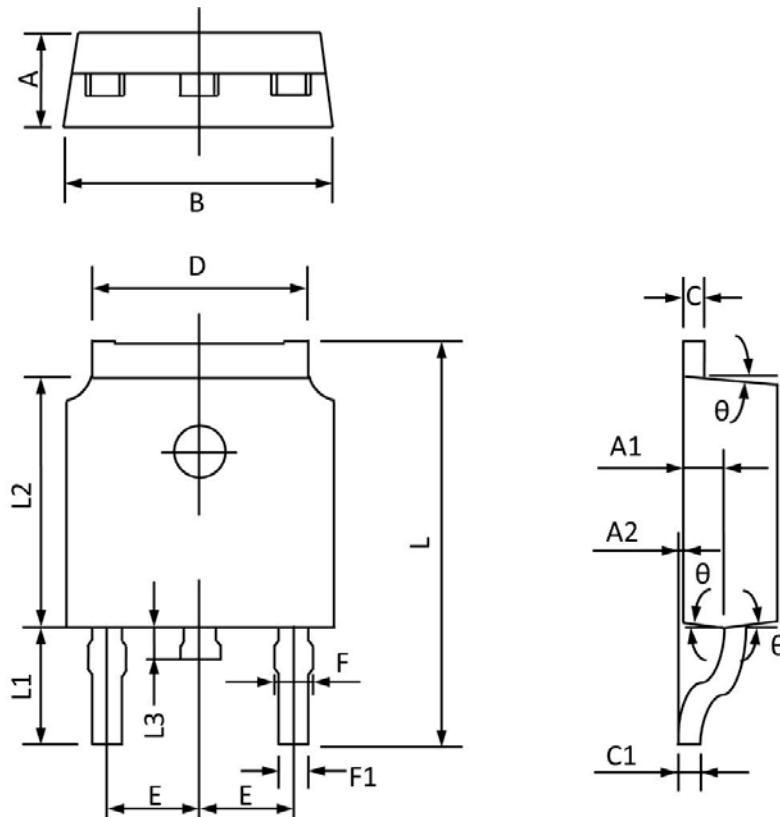


Fig.8 EAS Waveform

## TO252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.20	2.40	0.087	0.094
A1	0.91	1.11	0.036	0.044
A2	0.00	0.15	0.000	0.006
B	6.50	6.70	0.256	0.264
C	0.46	0.580	0.018	0.230
C1	0.46	0.580	0.018	0.030
D	5.10	5.46	0.201	0.215
E	2.186	2.386	0.086	0.094
F	0.74	0.94	0.029	0.037
F1	0.660	0.860	0.026	0.034
L	9.80	10.40	0.386	0.409
L1	2.9REF		0.114REF	
L2	6.00	6.20	0.236	0.244
L3	0.60	1.00	0.024	0.039
θ	3°	9°	3°	9°