

General Description

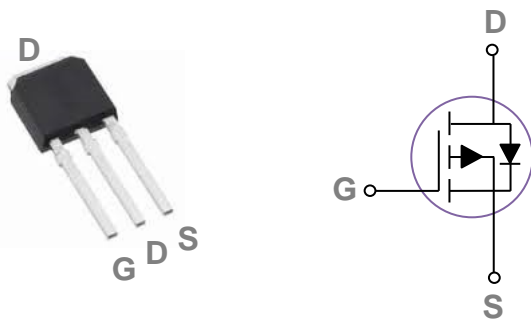
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	R _{DS(ON)}	I _D
-60V	68mΩ	-13A

Features

- -60V, -13A, R_{DS(ON)} = 68mΩ @ V_{GS} = -10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

TO251 Pin Configuration



Applications

- Motor Drive
- Power Tools
- LED Lighting

Absolute Maximum Ratings T_c=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-60	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (T _c =25°C)	-13	A
	Drain Current – Continuous (T _c =100°C)	-8	A
I _{DM}	Drain Current – Pulsed ¹	-52	A
EAS	Single Pulse Avalanche Energy ²	31	mJ
IAS	Single Pulse Avalanche Current ²	-25	A
P _D	Power Dissipation (T _c =25°C)	20	W
	Power Dissipation – Derate above 25°C	0.16	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	6.1	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-60	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	-0.05	---	V/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-60V, V _{GS} =0V, T _J =25°C	---	---	-1	uA
		V _{DS} =-48V, V _{GS} =0V, T _J =125°C	---	---	-10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-10V, I _D =-6A	---	54	68	mΩ
		V _{GS} =-4.5V, I _D =-3A	---	72	85	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1.2	-1.6	-2.2	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	5	---	mV/°C
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-6A	---	8.5	---	S

Dynamic and switching Characteristics

Q _g	Total Gate Charge ^{3, 4}	V _{DS} =-30V, V _{GS} =-10V, I _D =-6A	---	16.4	24	nC
Q _{gs}	Gate-Source Charge ^{3, 4}		---	2.8	5.6	
Q _{gd}	Gate-Drain Charge ^{3, 4}		---	3.6	7	
T _{d(on)}	Turn-On Delay Time ^{3, 4}	V _{DD} =-30V, V _{GS} =-10V, R _G =6Ω I _D =-6A	---	8.3	16	ns
T _r	Rise Time ^{3, 4}		---	29.6	56	
T _{d(off)}	Turn-Off Delay Time ^{3, 4}		---	51.7	98	
T _f	Fall Time ^{3, 4}		---	15.6	30	
C _{iss}	Input Capacitance	V _{DS} =-30V, V _{GS} =0V, F=1MHz	---	870	1260	pF
C _{oss}	Output Capacitance		---	70	100	
C _{rss}	Reverse Transfer Capacitance		---	42	60	
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	---	16	32	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	-13	A
I _{SM}	Pulsed Source Current		---	---	-52	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =-1A, T _J =25°C	---	---	-1	V
t _{rr}	Reverse Recovery Time	V _R =-50V, I _S =-10A	---	35	---	ns
Q _{rr}	Reverse Recovery Charge	di/dt=100A/μs, T _J =25°C	---	20	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-25A., R_G=25Ω, Starting T_J=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

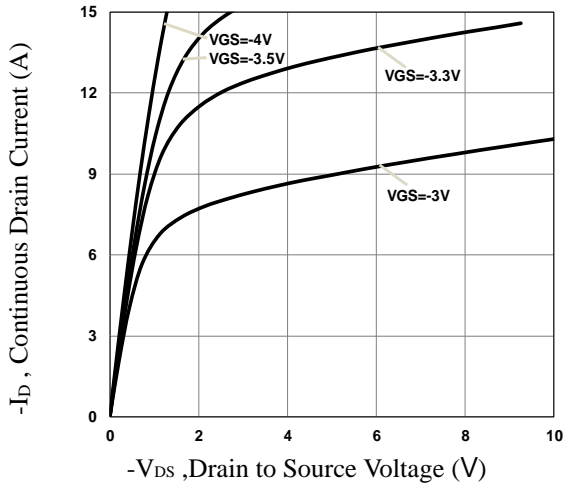


Fig.1 Typical Output Characteristics

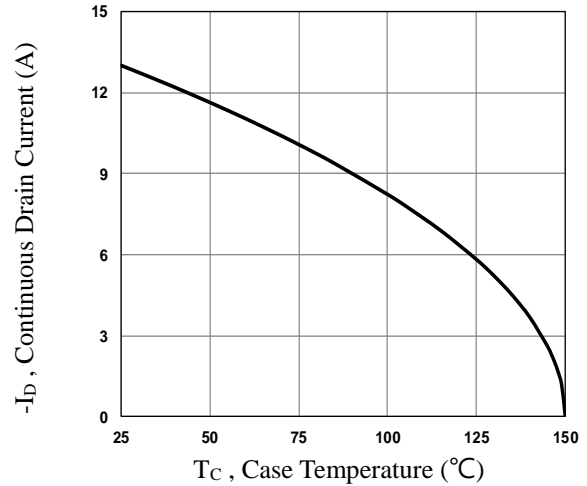


Fig.2 Continuous Drain Current vs. T_c

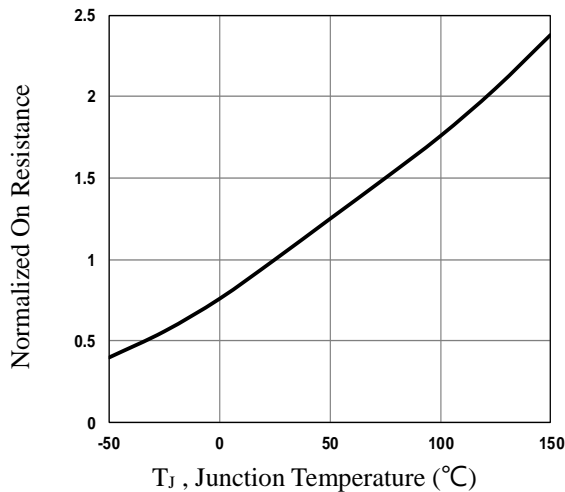


Fig.3 Normalized R_{DS(on)} vs. T_j

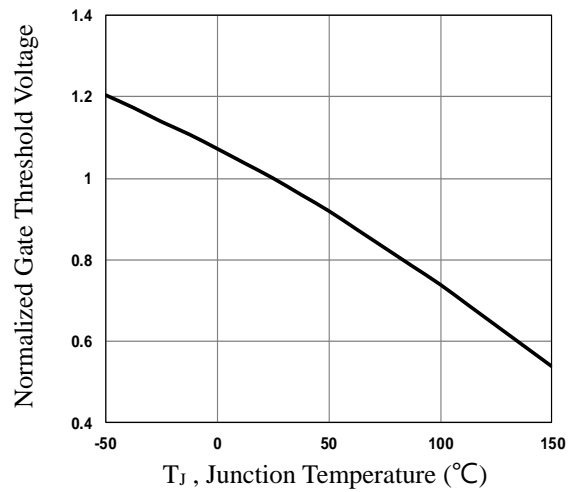


Fig.4 Normalized V_{th} vs. T_j

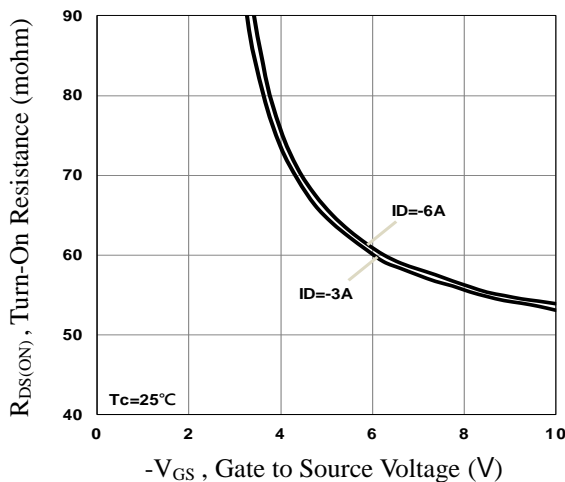


Fig.5 Turn-On Resistance vs. V_{GS}

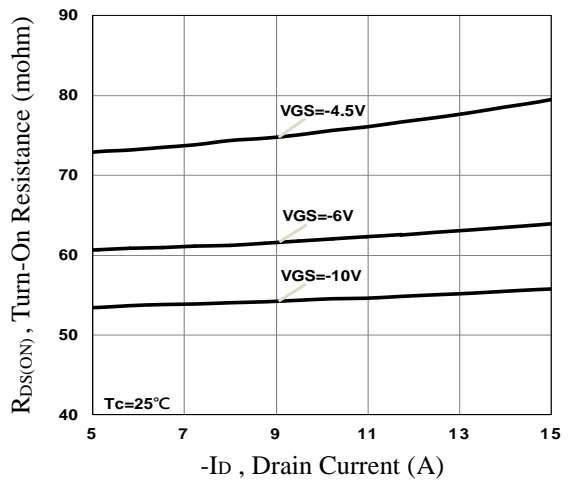


Fig.6 Turn-On Resistance vs. I_D

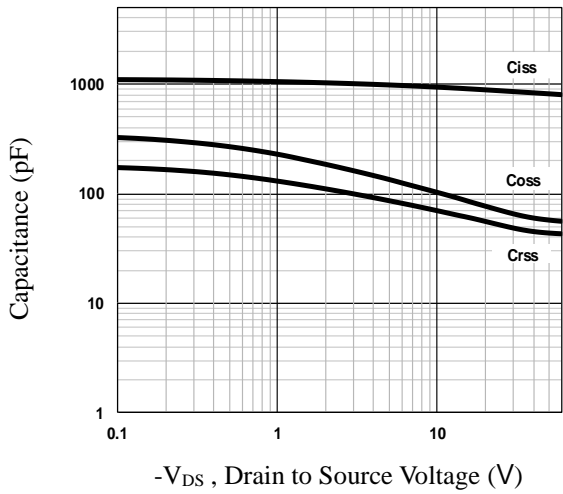


Fig.7 Capacitance Characteristics

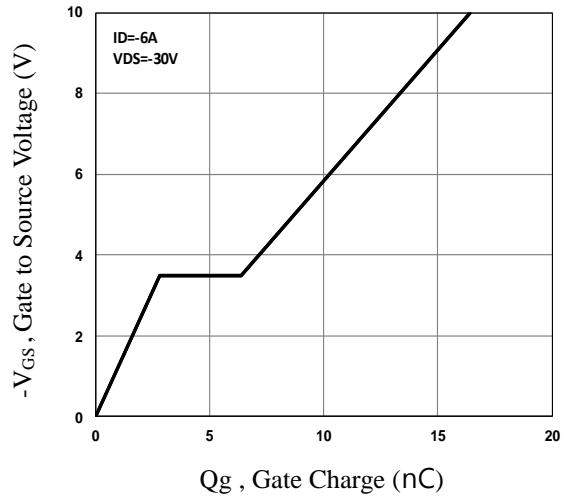


Fig.8 Gate Charge Characteristics

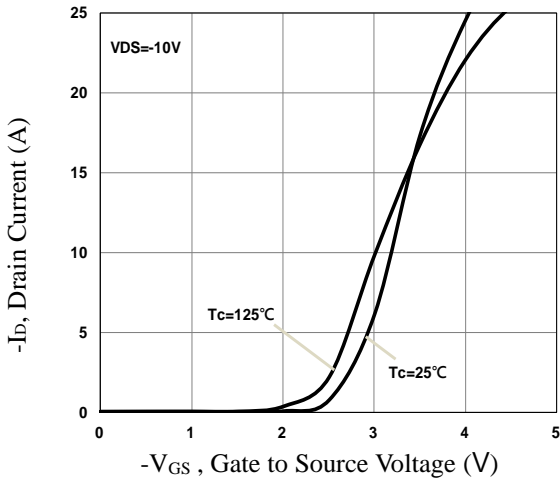


Fig.9 Transfer Characteristics

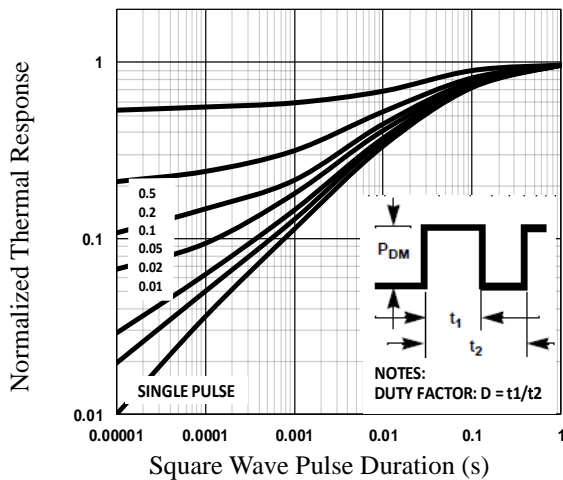


Fig.10 Normalized Transient Impedance

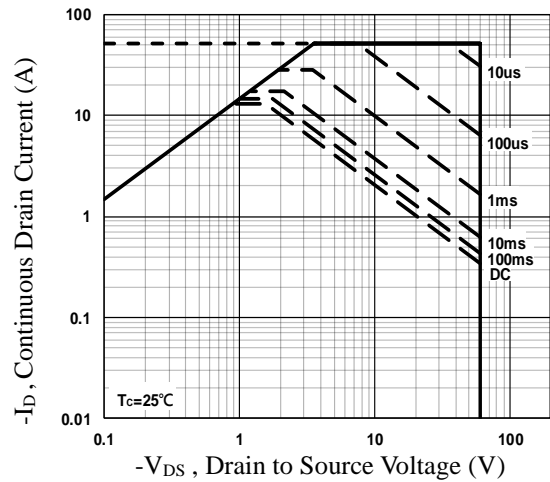
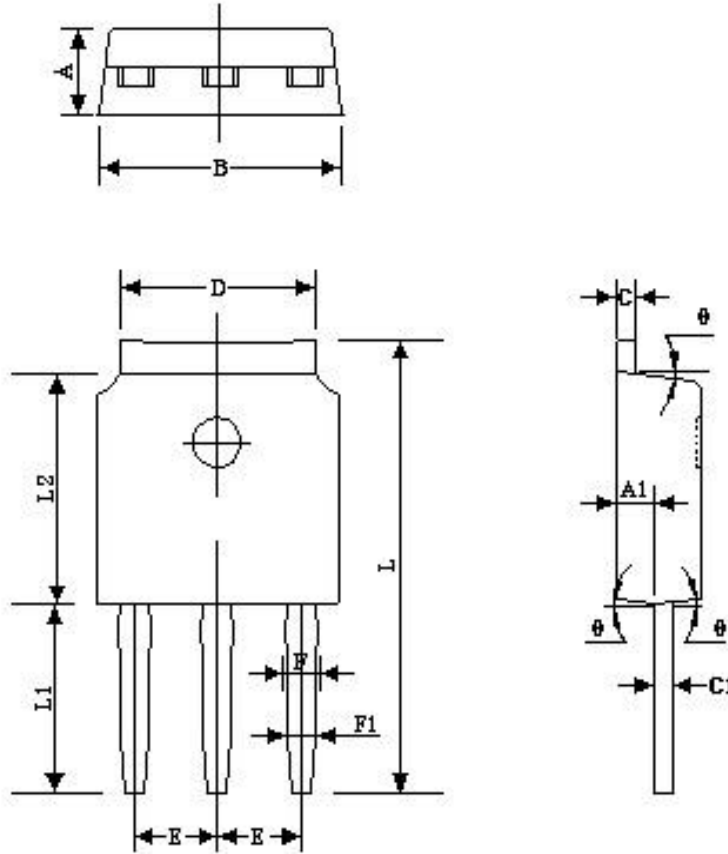


Fig.11 Maximum Safe Operation Area

TO251 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.500	2.100	0.098	0.083
A1	1.250	0.900	0.049	0.035
B	6.800	6.400	0.268	0.252
C	0.580	0.420	0.023	0.017
C1	0.580	0.420	0.023	0.017
D	5.500	5.000	0.217	0.197
E	2.400	2.000	0.094	0.079
F	1.050	0.750	0.041	0.030
F1	0.900	0.650	0.035	0.026
L	12.400	11.600	0.488	0.457
L1	5.300	4.700	0.209	0.185
L2	6.300	5.700	0.248	0.224
θ	9°	3°	9°	3°