

The XDXL62004 Series are high-voltage, high-current darlington drivers comprised of seven NPN darlington pairs. All units feature integral clamp diodes for switching inductive loads.

Applications include relay, hammer, lamp and display (LED) drivers.

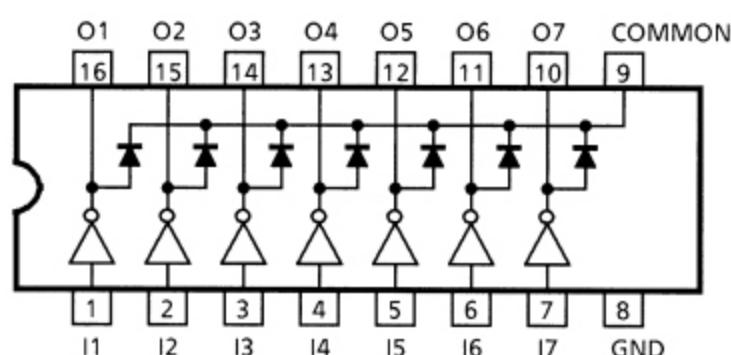
The suffix (G) appended to the part number represents a Lead (Pb)-Free product.

## Features

- Output current (single output): 500 mA max
- High sustaining voltage output: 50 V min
- Output clamp diodes
- Inputs compatible with various types of logic

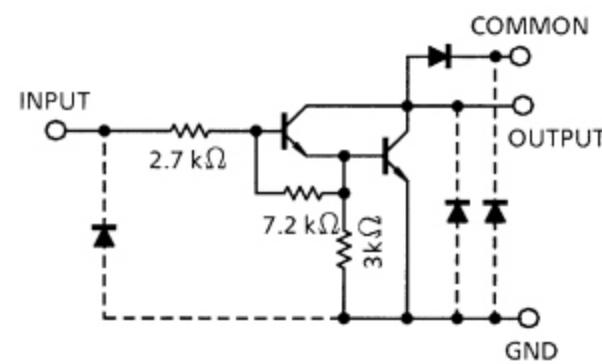
Type	Input Base Resistor	Designation
XDXL62004	2.7 kΩ	TTL, 5 V CMOS

## Pin Connection (top view)



## Schematics (each driver)

XDXL62004



Note: The input and output parasitic diodes cannot be used as clamp diodes.

## Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

Characteristic	Symbol	Rating	Unit
Output sustaining voltage	$V_{CE(\text{SUS})}$	-0.5~50	V
Output current	$I_{\text{OUT}}$	500	mA/ch
Input voltage	$V_{\text{IN}}$	-0.5~30	V
Clamp diode reverse voltage	$V_R$	50	V
Clamp diode forward current	$I_F$	500	mA
Power dissipation	$P_D$	1.47	W
		1.25 (Note)	
Operating temperature	$T_{\text{opr}}$	-40~85	°C
Storage temperature	$T_{\text{stg}}$	-55~150	°C

Note: On PCB (Test Board: JEDEC 2s2p)

**Recommended Operating Conditions ( $T_a = -40$  to  $85^\circ\text{C}$ )**

Characteristic		Symbol	Test Condition		Min	Typ.	Max	Unit		
Output sustaining voltage		$V_{CE(\text{SUS})}$			0	—	50	V		
Output current	XD62004	$I_{\text{OUT}}$	$T_{\text{pw}} = 25 \text{ ms}$ 7 Circuits $T_a = 85^\circ\text{C}$ $T_j = 120^\circ\text{C}$	Duty = 10%	0	—	350	mA/ch		
				Duty = 50%	0	—	100			
	XL62004			Duty = 10%	0	—	300			
				Duty = 50%	0	—	90			
Input voltage		$V_{IN}$			0	—	24	V		
Input voltage (output on)	XDXL62004	$V_{IN(\text{ON})}$	$I_{\text{OUT}} = 400 \text{ mA}$ $h_{FE} = 800$	2.8	—	24	V			
				—	—	—				
Input voltage (output off)	XDXL62004	$V_{IN(\text{OFF})}$		0	—	0.7	V			
				—	—	—				
Clamp diode reverse voltage		$V_R$			—	—	50	V		
Clamp diode forward current		$I_F$			—	—	350	mA		
Power dissipation	XD62004	$P_D$	$T_a = 85^\circ\text{C}$ $T_a = 85^\circ\text{C}$ (Note)	—	—	0.76	W			
	XL62004			—	—	0.65				

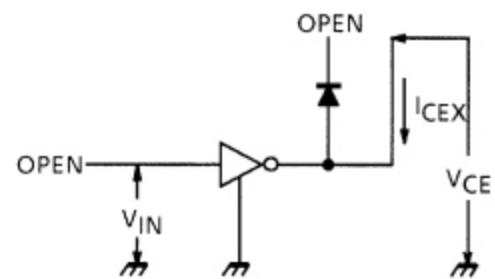
Note: On PCB (Test Board: JEDEC 2s2p)

**Electrical Characteristics ( $T_a = 25^\circ\text{C}$  unless otherwise noted)**

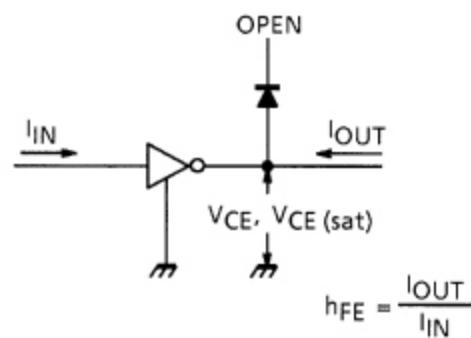
Characteristic		Symbol	Test Circuit	Test Condition		Min	Typ.	Max	Unit	
Output leakage current		$I_{CEX}$	1	$V_{CE} = 50 \text{ V}, T_a = 25^\circ\text{C}$		—	—	50	$\mu\text{A}$	
				$V_{CE} = 50 \text{ V}, T_a = 85^\circ\text{C}$		—	—	100		
Collector-emitter saturation voltage		$V_{CE(\text{sat})}$	2	$I_{\text{OUT}} = 350 \text{ mA}, I_{IN} = 500 \mu\text{A}$		—	1.3	1.6	V	
				$I_{\text{OUT}} = 200 \text{ mA}, I_{IN} = 350 \mu\text{A}$		—	1.1	1.3		
				$I_{\text{OUT}} = 100 \text{ mA}, I_{IN} = 250 \mu\text{A}$		—	0.9	1.1		
DC Current transfer ratio		$h_{FE}$	2	$V_{CE} = 2 \text{ V}, I_{\text{OUT}} = 350 \text{ mA}$		1000	—	—		
Input current (output on)	XDXL62004	$I_{IN(\text{ON})}$	3	$V_{IN} = 2.4 \text{ V}, I_{\text{OUT}} = 350 \text{ mA}$		—	0.4	0.7	mA	
				—		—	—	—		
Input current (output off)		$I_{IN(\text{OFF})}$	4	$I_{\text{OUT}} = 500 \mu\text{A}, T_a = 85^\circ\text{C}$		50	65	—	$\mu\text{A}$	
Input voltage (output on)	XDXL62004	$V_{IN(\text{ON})}$	5	$V_{CE} = 2 \text{ V}$ $h_{FE} = 800$	$I_{\text{OUT}} = 350 \text{ mA}$	—	—	2.6	V	
					$I_{\text{OUT}} = 200 \text{ mA}$	—	—	2.0		
Clamp diode reverse current		$I_R$	6	$V_R = 50 \text{ V}, T_a = 25^\circ\text{C}$		—	—	50	$\mu\text{A}$	
				$V_R = 50 \text{ V}, T_a = 85^\circ\text{C}$		—	—	100		
Clamp diode forward voltage		$V_F$	7	$I_F = 350 \text{ mA}$		—	—	2.0	V	
Input capacitance		$C_{IN}$	—			—	15	—	pF	
Turn-on delay		$t_{ON}$	8	$V_{OUT} = 50 \text{ V}, R_L = 125 \Omega$ $C_L = 15 \text{ pF}$		—	0.1	—	$\mu\text{s}$	
Turn-off delay		$t_{OFF}$	8	$V_{OUT} = 50 \text{ V}, R_L = 125 \Omega$ $C_L = 15 \text{ pF}$		—	0.2	—		

### Test Circuit

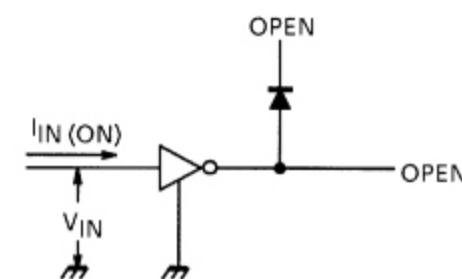
1.  $I_{CEX}$



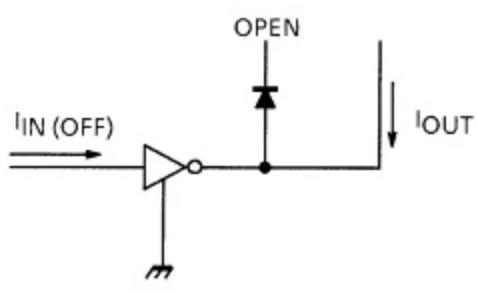
2.  $V_{CE(\text{sat})}, h_{FE}$



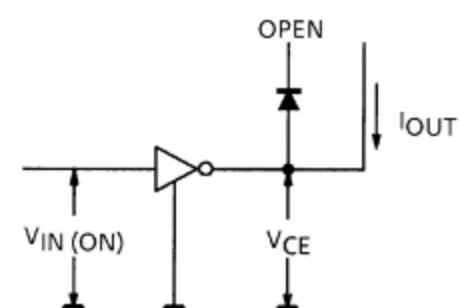
3.  $I_{IN (\text{ON})}$



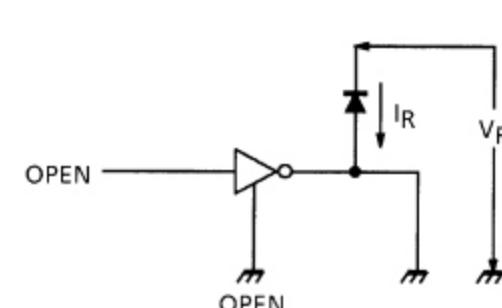
4.  $I_{IN (\text{OFF})}$



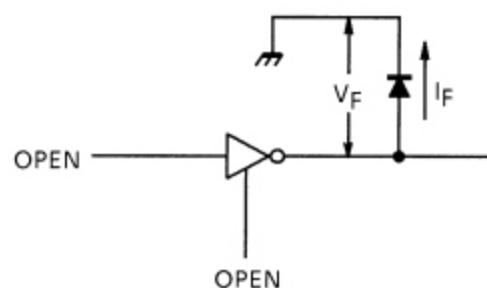
5.  $V_{IN (\text{ON})}$



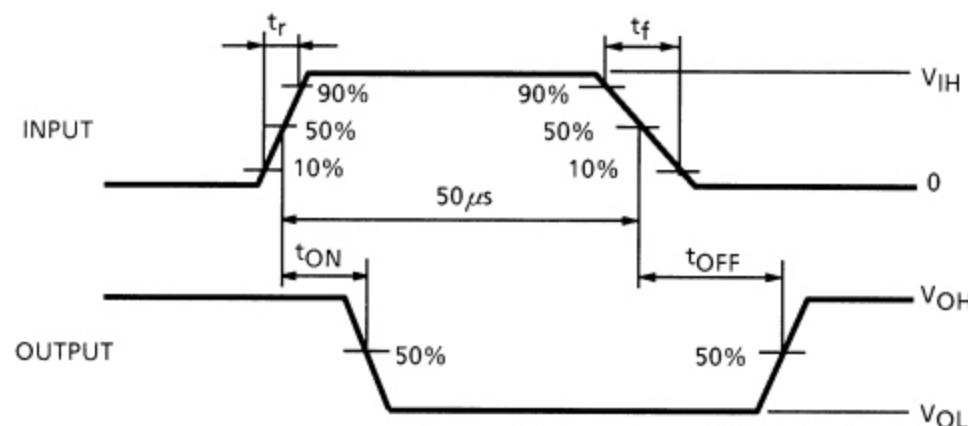
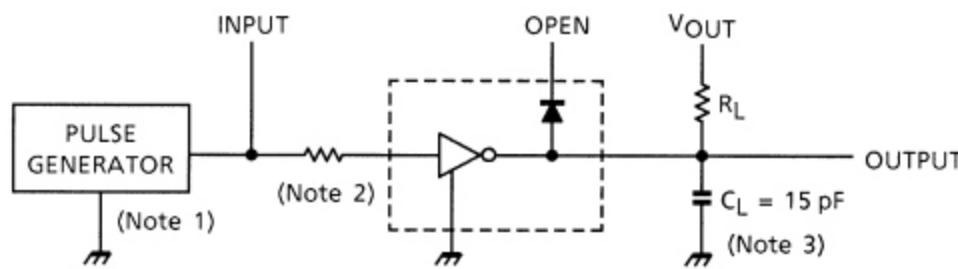
6.  $I_R$



7.  $V_F$



## 8. $t_{ON}$ , $t_{OFF}$



Note 1: Pulse width 50  $\mu$ s, duty cycle 10%  
 Output impedance 50  $\Omega$ ,  $t_r \leq 5$  ns,  $t_f \leq 10$  ns

Note 2: See below

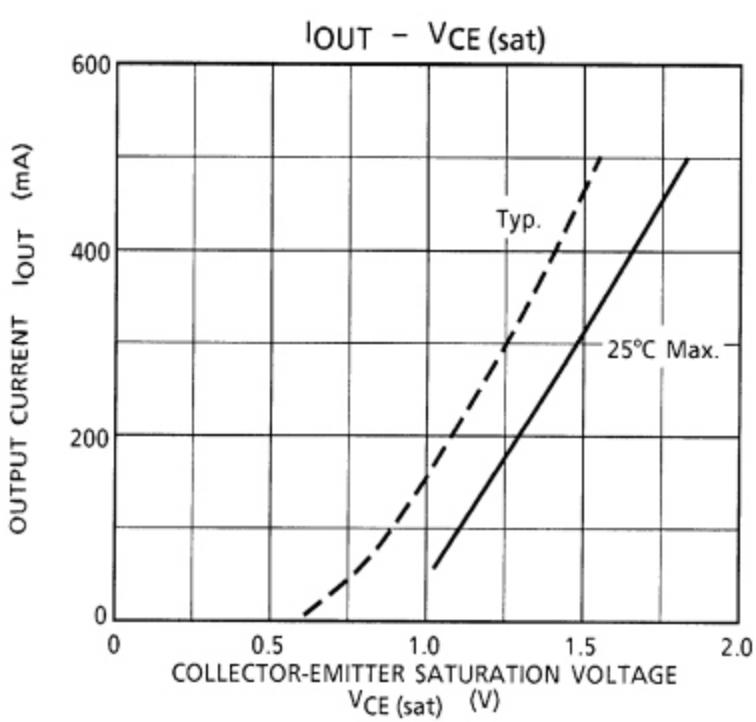
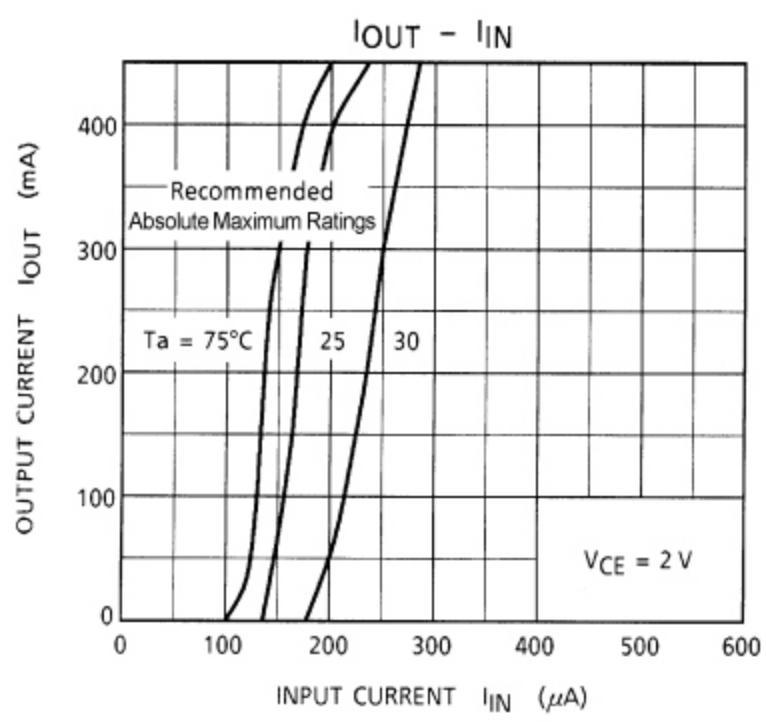
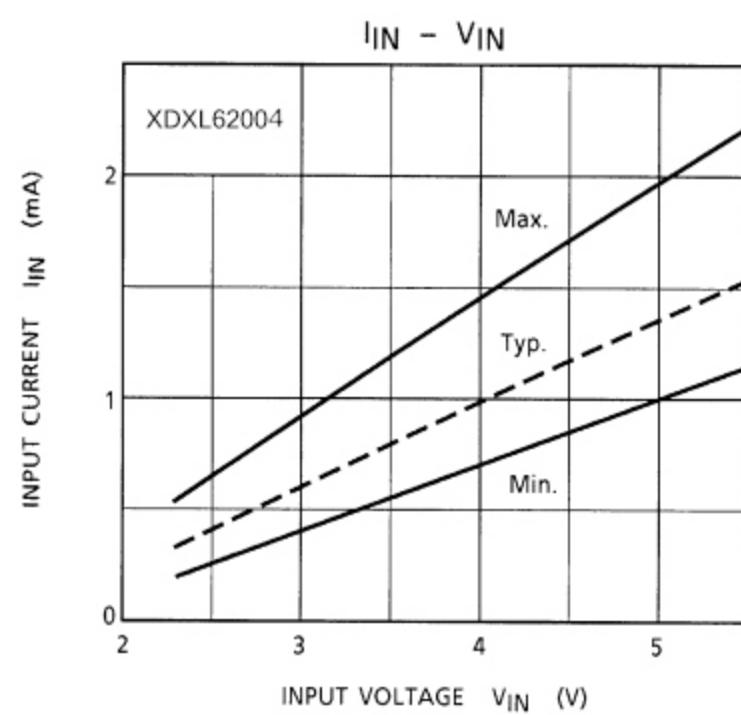
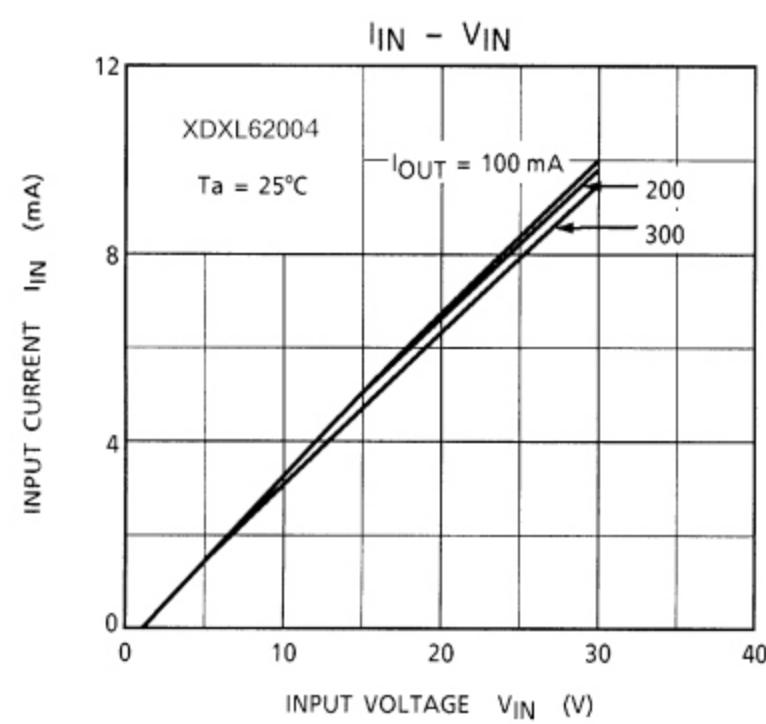
### Input Condition

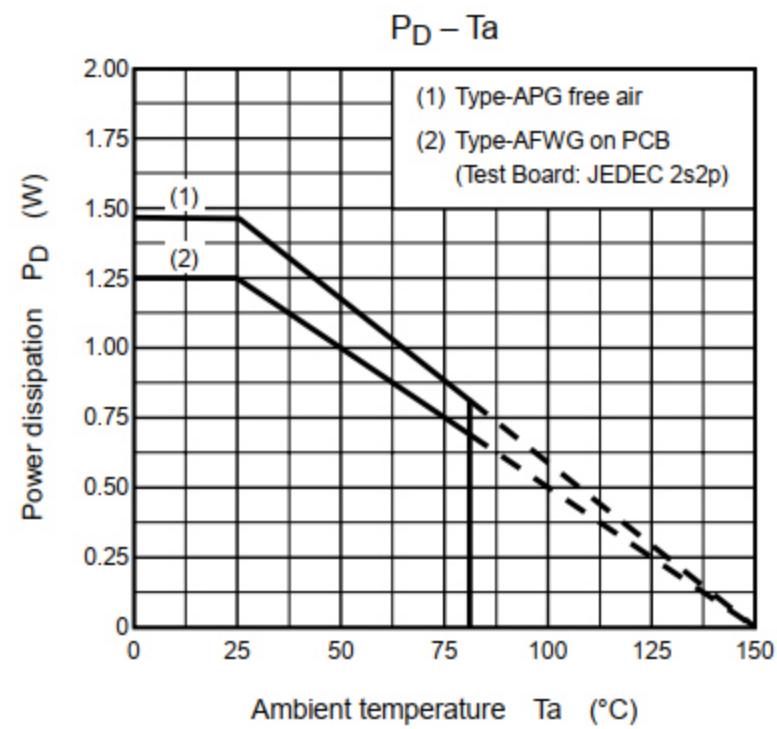
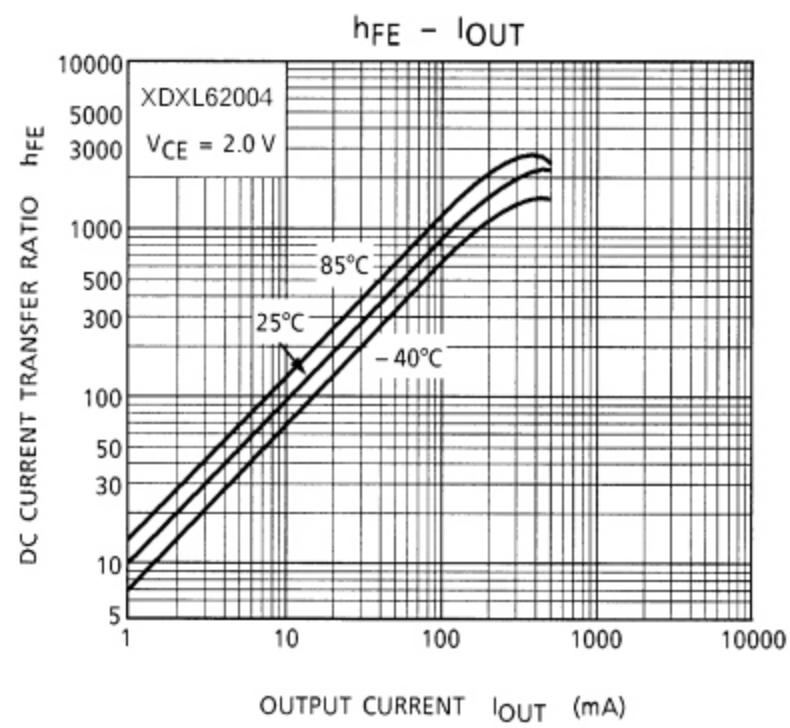
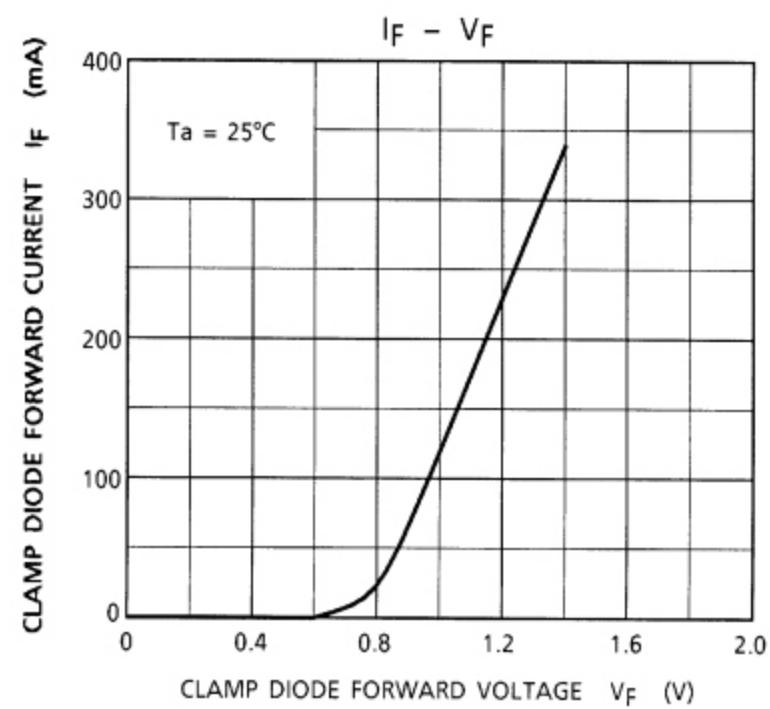
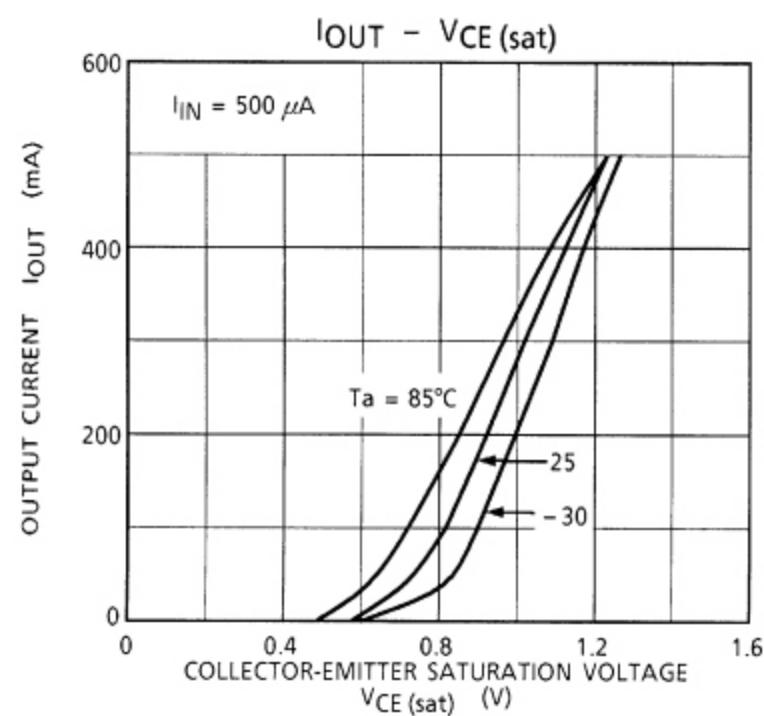
Type Number	R1	$V_{IH}$
XDXL62004	0	3 V

Note 3:  $C_L$  includes probe and jig capacitance.

## Precautions for Using

This IC does not include built-in protection circuits for excess current or overvoltage.  
 If this IC is subjected to excess current or overvoltage, it may be destroyed.  
 Hence, the utmost care must be taken when systems which incorporate this IC are designed.  
 Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



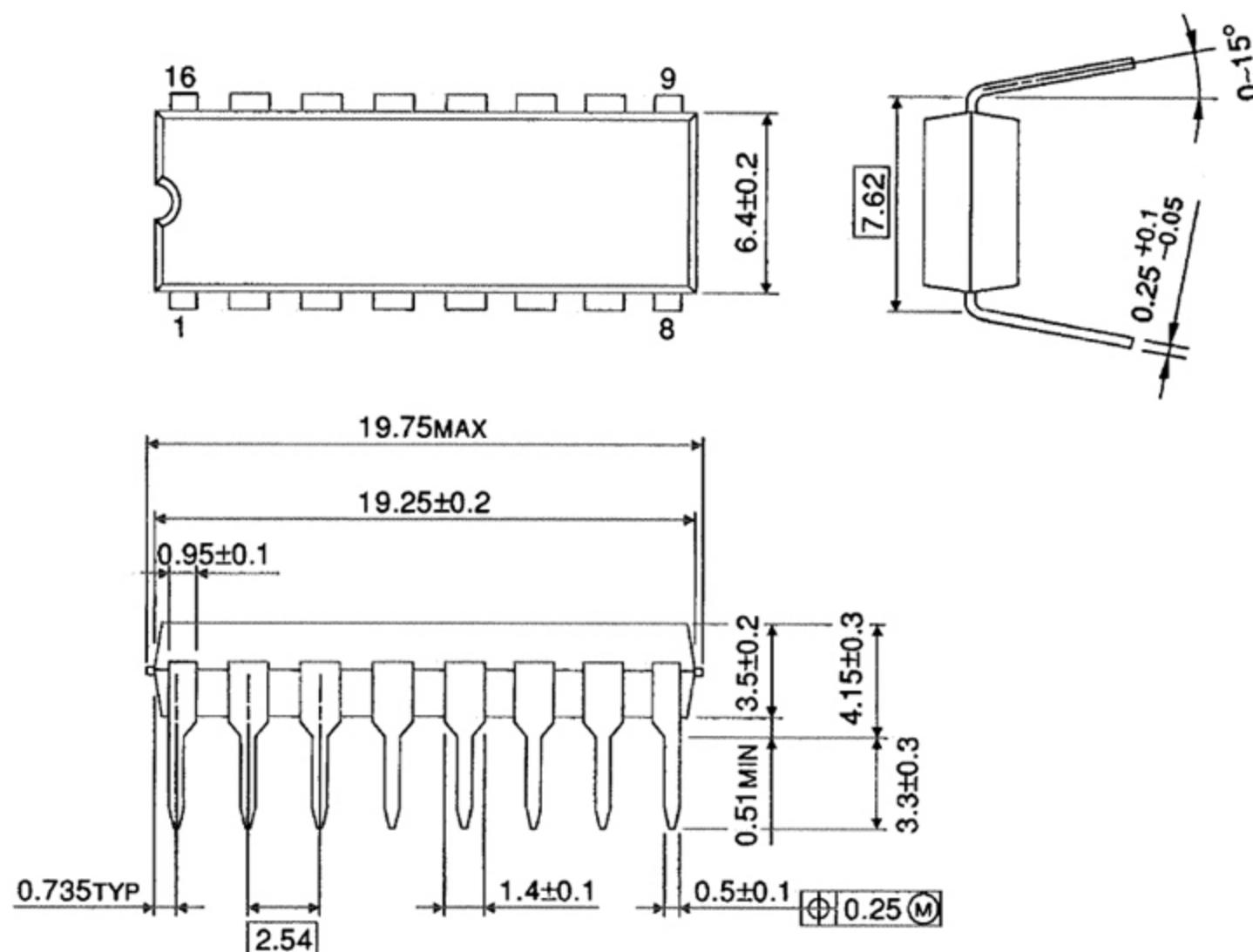


**XL62004 SOP16**  
**XD62004 DIP16**

### Package Dimensions

DIP16

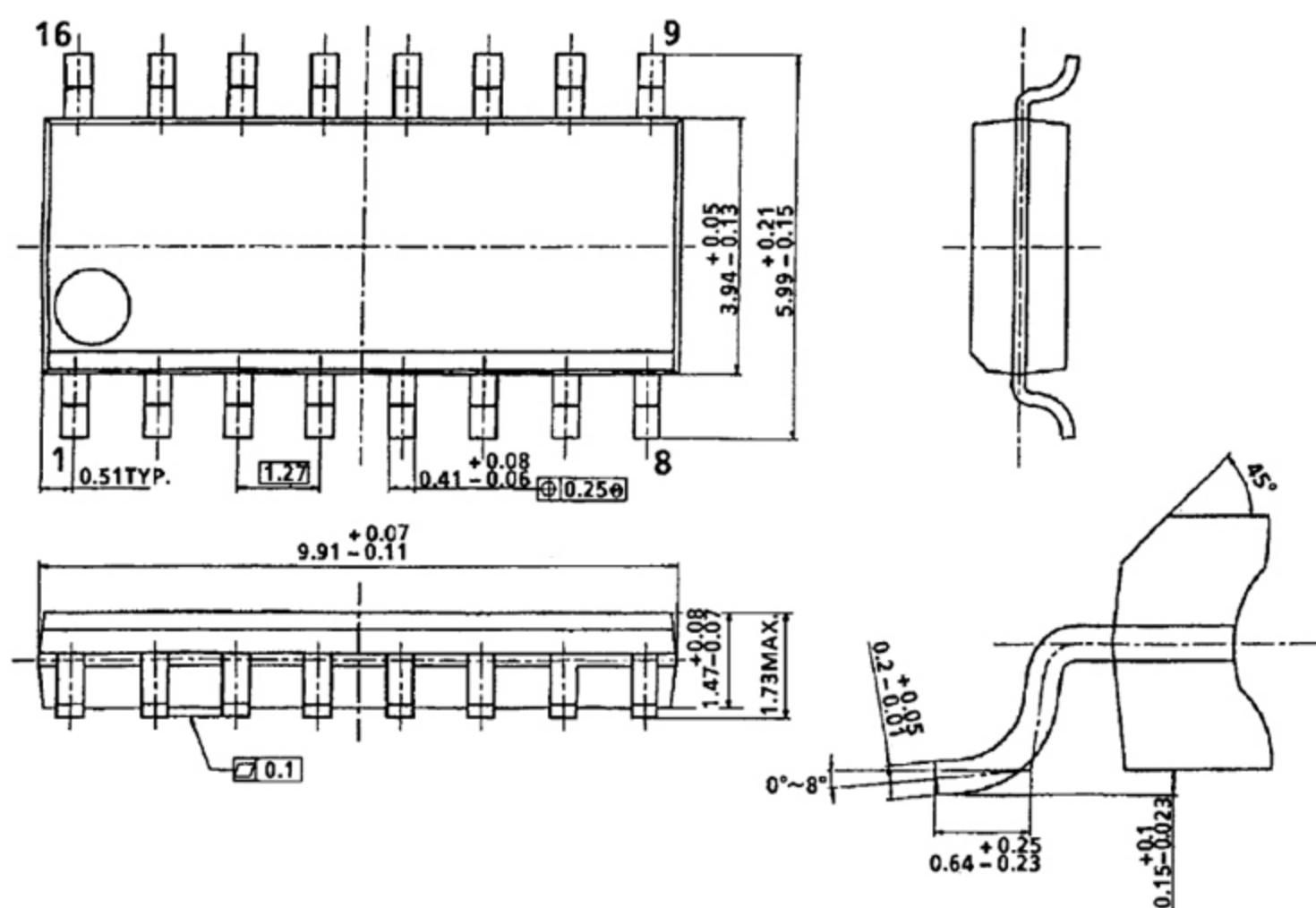
Unit : mm



### Package Dimensions

SOP16

Unit : mm



以上信息仅供参考. 如需帮助联系客服人员。谢谢 XINLUDA