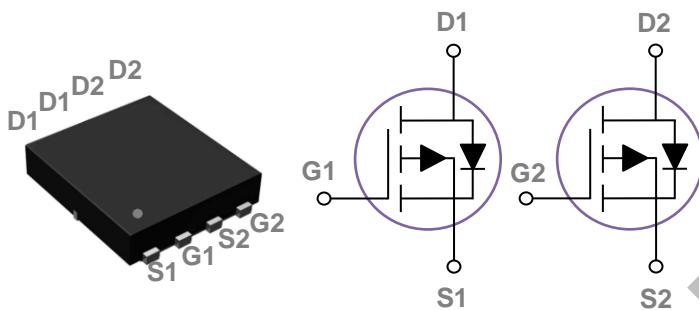


### General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### PPAK5x6 Dual Pin Configuration



BVDSS	RDS(ON)	ID
-60V	68mΩ	-15A

### Features

- -60V, -15A, RDS(ON) = 68mΩ@VGS = -10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Motor Drive
- Power Tools
- LED Lighting

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-60	V
V <sub>Gs</sub>	Gate-Source Voltage	$\pm 20$	V
I <sub>D</sub>	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	-15	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	-9.5	A
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	-60	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	31	mJ
I <sub>AS</sub>	Single Pulse Avalanche Current <sup>2</sup>	-25	A
P <sub>D</sub>	Power Dissipation ( $T_c=25^\circ\text{C}$ )	33	W
	Power Dissipation – Derate above 25°C	0.26	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to ambient	---	62.5	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction to Case	---	3.8	°C/W

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=-250\mu\text{A}$	-60	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-60\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	$\mu\text{A}$
		$V_{DS}=-48\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125\text{ }^{\circ}\text{C}$	---	---	-10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=-10\text{V}$ , $I_D=-8\text{A}$	---	54	68	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$ , $I_D=-6\text{A}$	---	72	85	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250\mu\text{A}$	-1.2	-1.6	-2.5	V
$g_{fs}$	Forward Transconductance	$V_{DS}=-10\text{V}$ , $I_D=-3\text{A}$	---	8.5	---	S

**Dynamic and switching Characteristics**

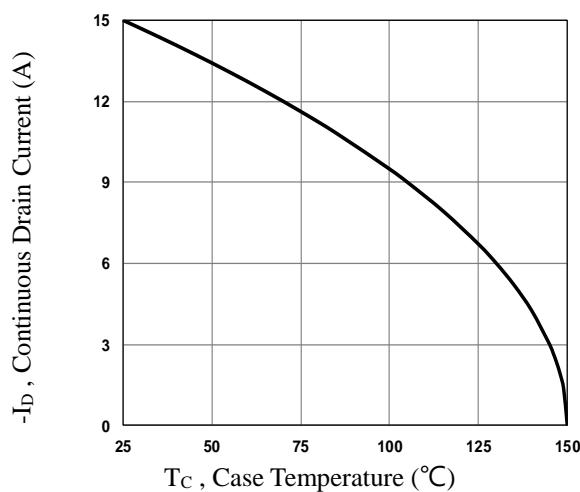
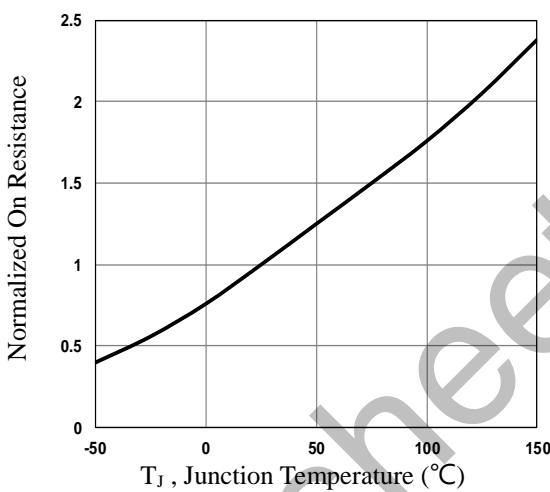
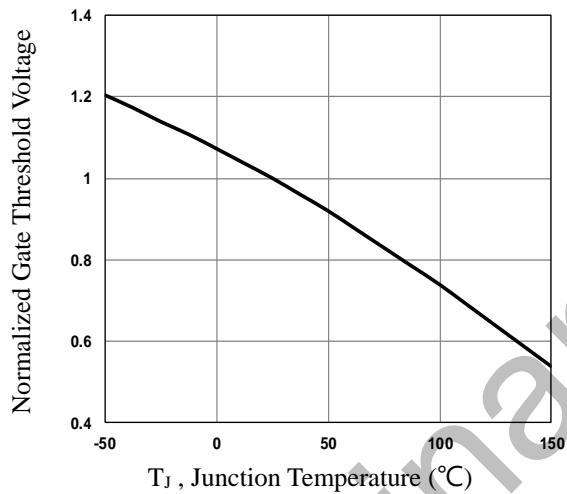
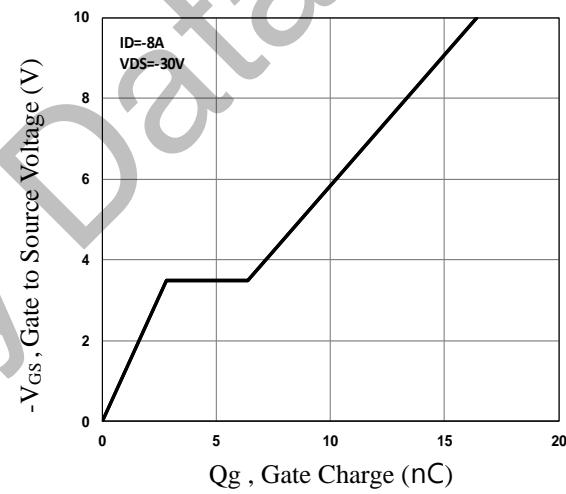
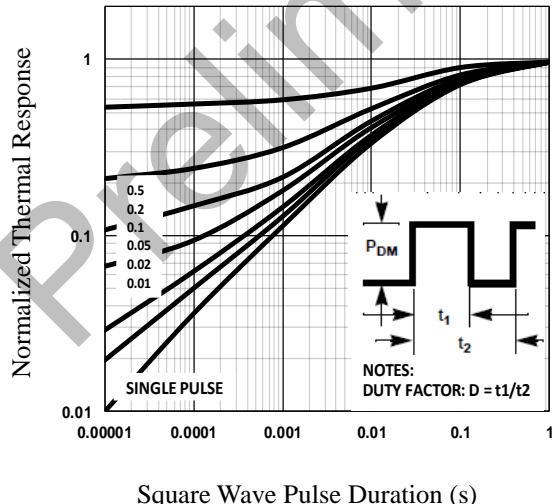
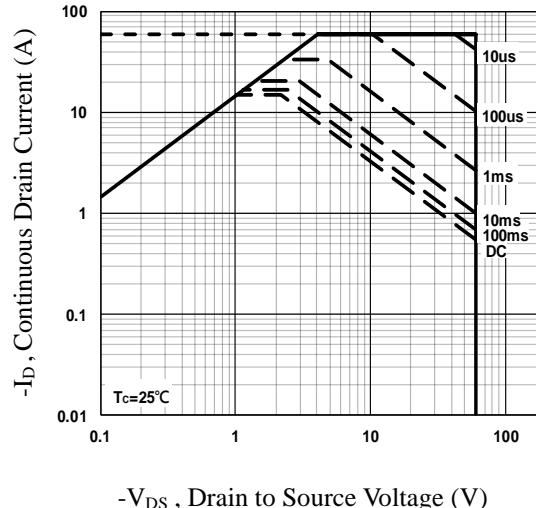
$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{DS}=-30\text{V}$ , $V_{GS}=-10\text{V}$ , $I_D=-8\text{A}$	---	16.4	25	nC
$Q_{gs}$	Gate-Source Charge <sup>3, 4</sup>		---	2.8	4.2	
$Q_{gd}$	Gate-Drain Charge <sup>3, 4</sup>		---	3.6	5.4	
$T_{d(on)}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{DD}=-30\text{V}$ , $V_{GS}=-10\text{V}$ , $R_G=6\Omega$ $I_D=-8\text{A}$	---	8.3	13	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	29.6	45	
$T_{d(off)}$	Turn-Off Delay Time <sup>3, 4</sup>		---	51.7	78	
$T_f$	Fall Time <sup>3, 4</sup>		---	15.6	24	
$C_{iss}$	Input Capacitance	$V_{DS}=-30\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	870	1305	pF
$C_{oss}$	Output Capacitance		---	70	105	
$C_{rss}$	Reverse Transfer Capacitance		---	42	63	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	16	---	$\Omega$

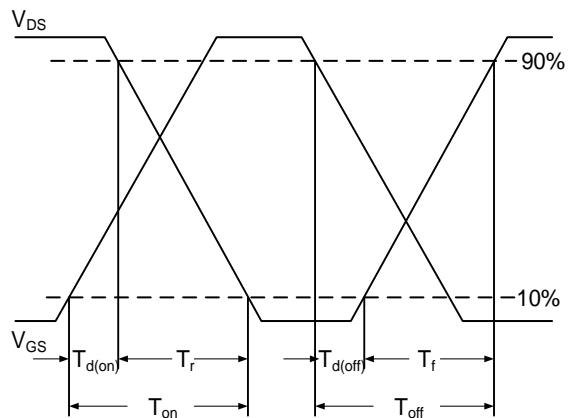
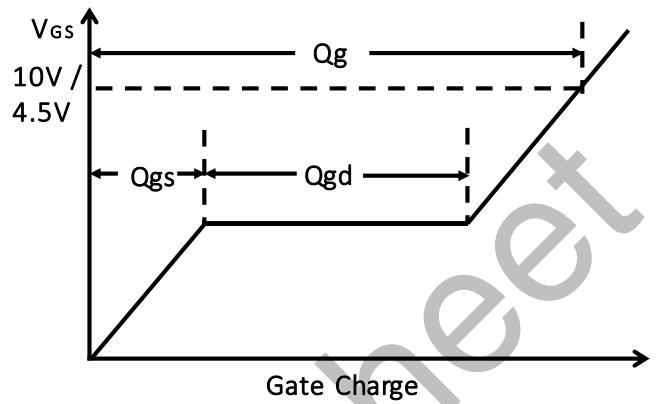
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-15	A
			---	---	-30	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=-1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	V
$t_{rr}$	Reverse Recovery Time	$V_R=-50\text{V}$ , $I_s=-10\text{A}$	---	35	---	ns
$Q_{rr}$	Reverse Recovery Charge	$di/dt=100\text{A}/\mu\text{s}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	20	---	nC

Note :

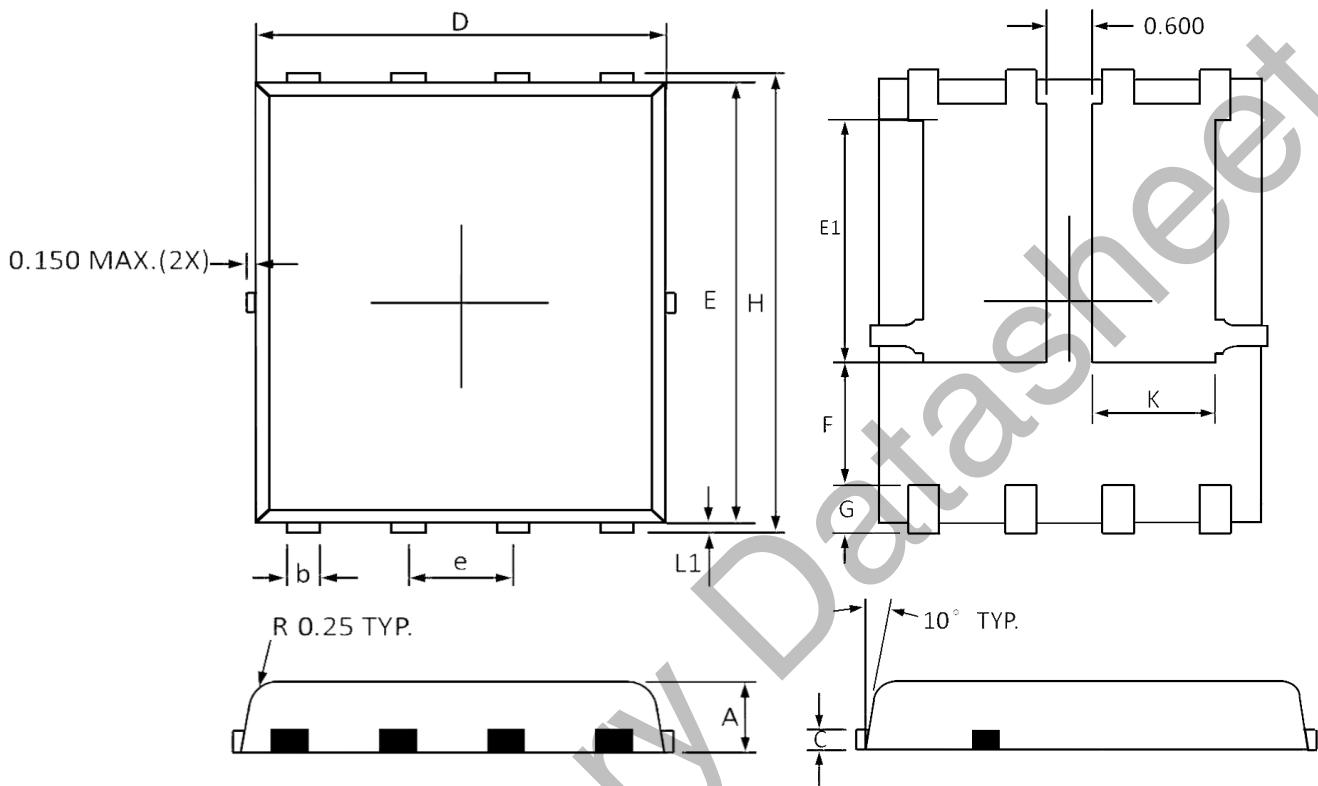
- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- $V_{DD}=-25\text{V}$ ,  $V_{GS}=-10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=-25\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25\text{ }^{\circ}\text{C}$ .
- The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
- Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**

**Fig.2 Normalized RDS(on) vs. T<sub>j</sub>**

**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 Gate Charge Waveform**

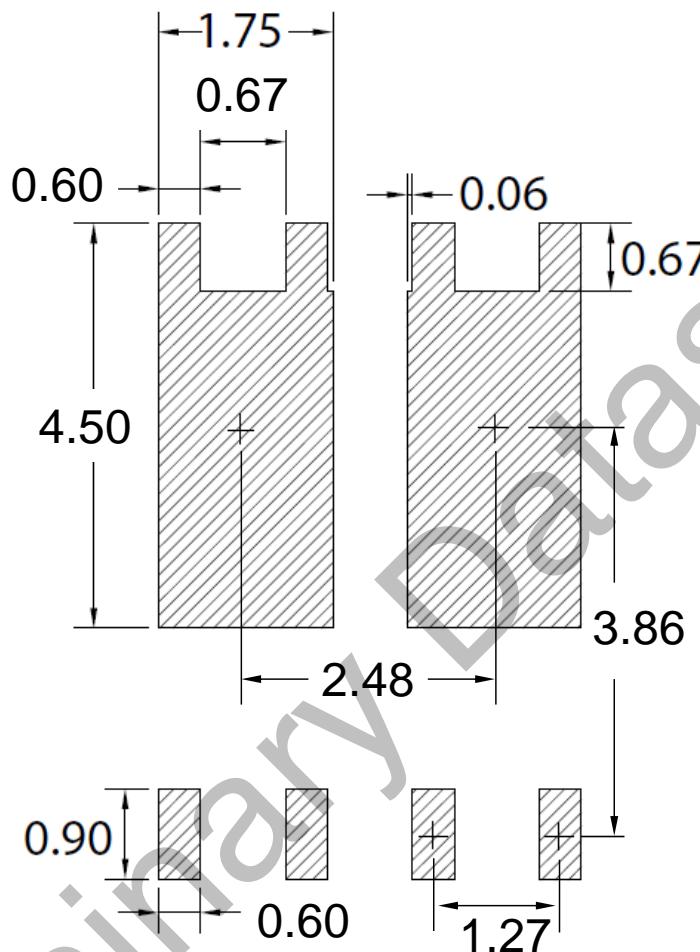
Preliminary Datasheet

## PPAK5x6 Dual PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.800	1.200	0.031	0.047
b	0.300	0.510	0.012	0.020
C	0.250 Ref		0.010 Ref	
D	4.800	5.400	0.189	0.213
E	5.450	5.960	0.215	0.235
E1	3.200	3.800	0.126	0.150
e	1.27 BSC		0.050 BSC	
F	1.000	1.900	0.039	0.075
G	0.380	0.800	0.015	0.031
H	5.850	6.300	0.230	0.248
L1	0.050	0.250	0.002	0.010
K	1.500	1.900	0.059	0.074

## PPAK5X6 Dual RECOMMENDED LAND PATTERN



unit : mm