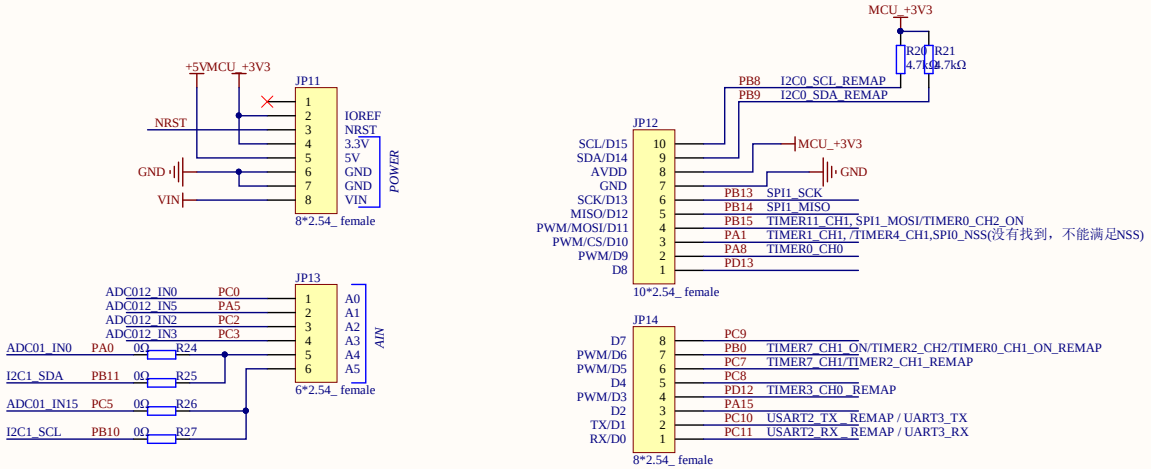
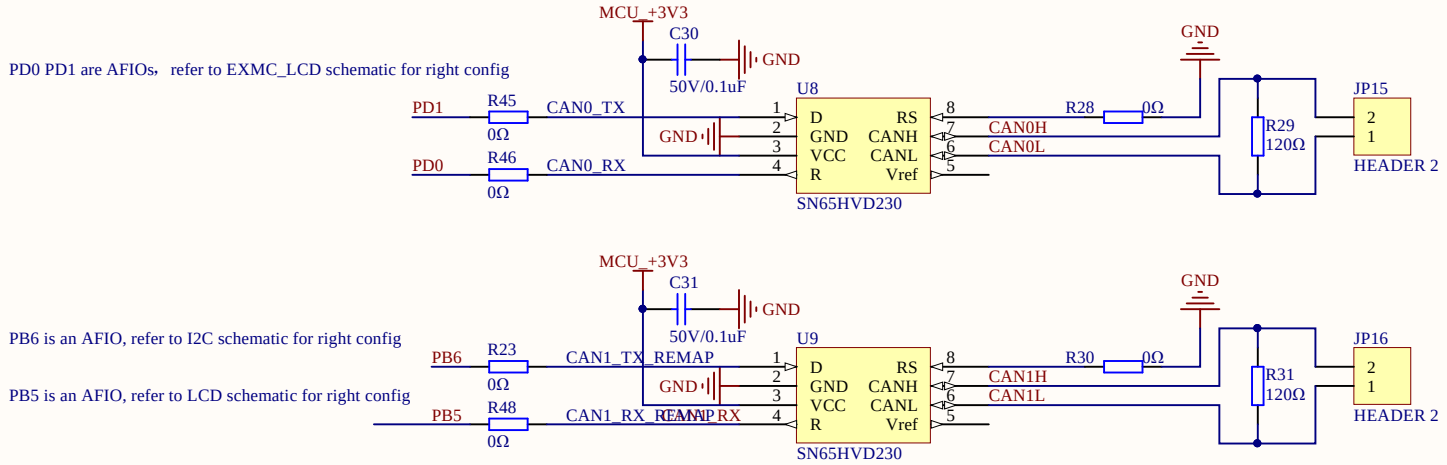
Arduino uno connector

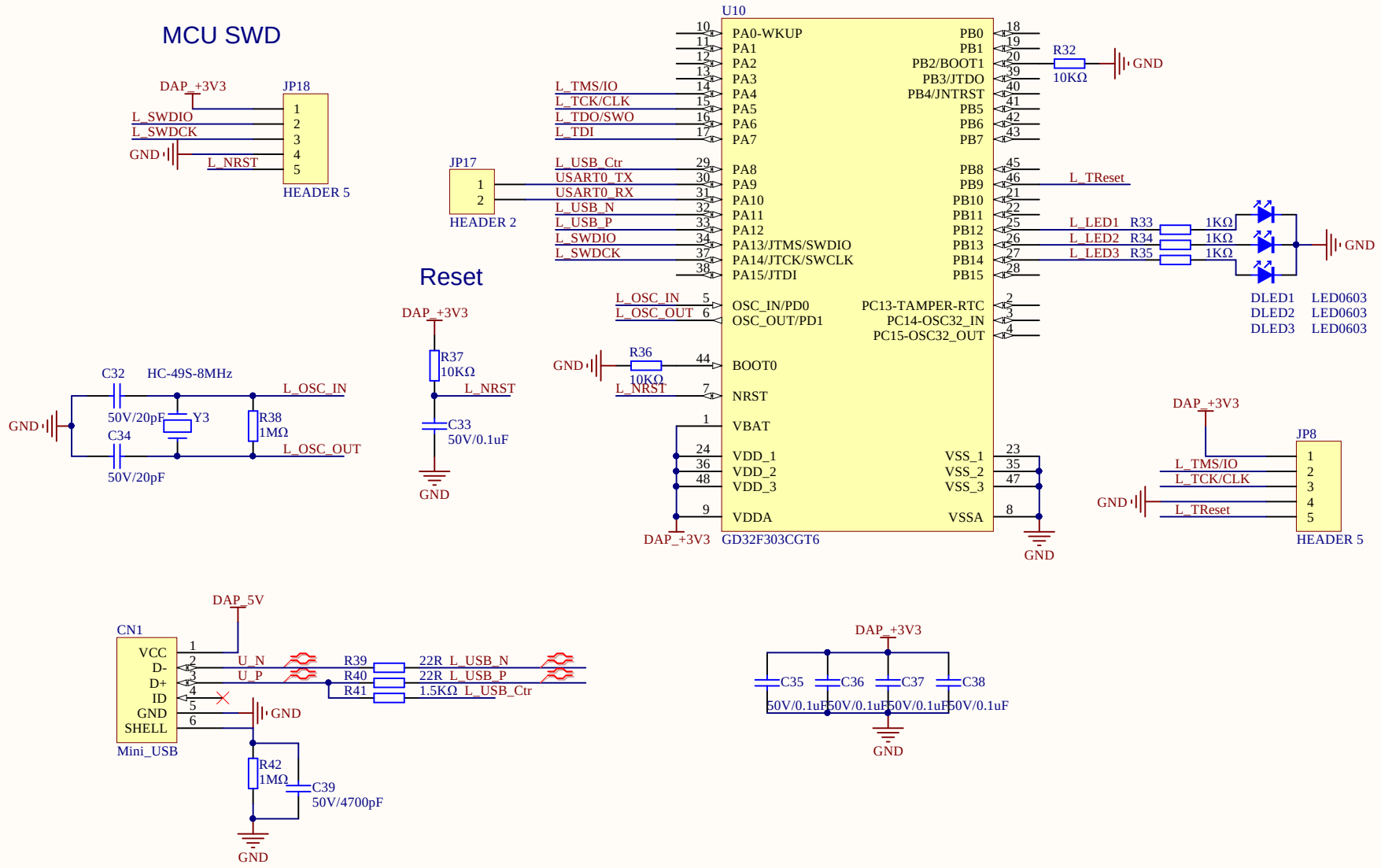


Company Name: GigaDevice		
File Name: Arduino		
Revision: 1.0	Data: 2018-10	Author: kywang

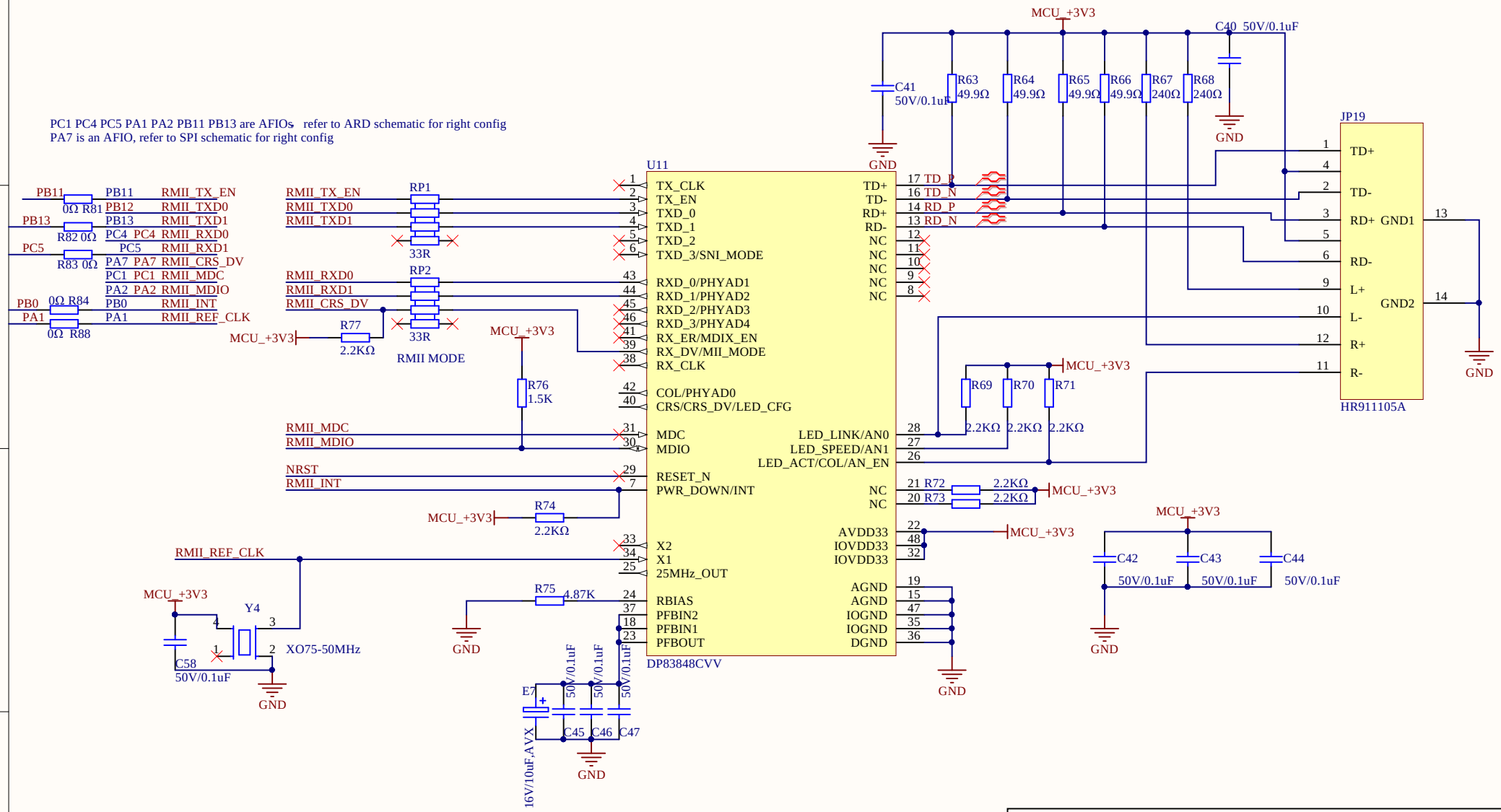
CAN

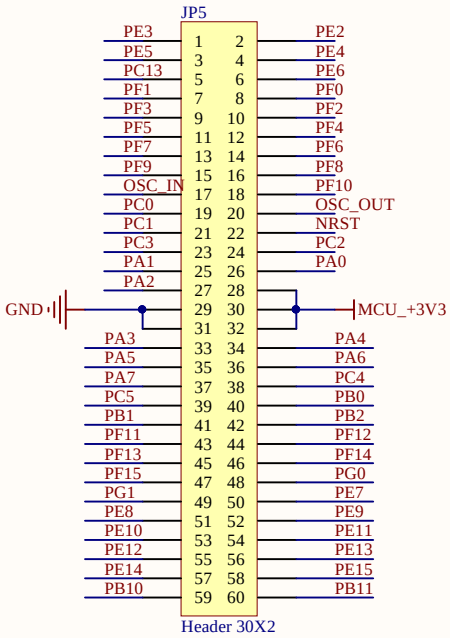


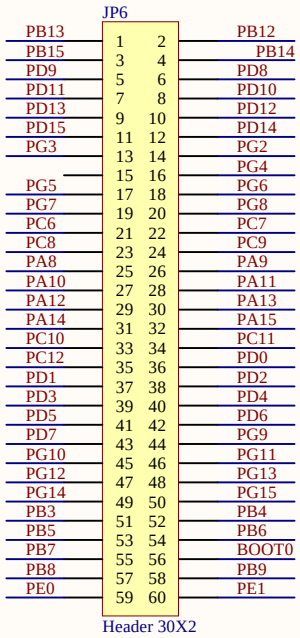
Company Name: GigaDevice		
File Name: CAN		
Revision: 1.0	Data: 2018-10	Author: kywang



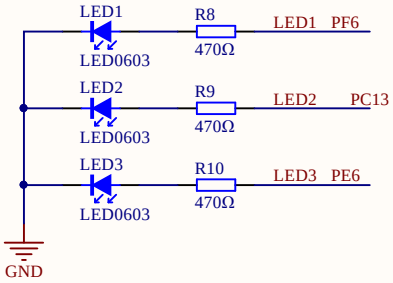
Company Name: GigaDevice		
File Name: DAPLink		
Revision: 1.0	Data: 2018-10	Author: kywang


PC1 PC4 PC5 PA1 PA2 PB11 PB13 are AFIOs, refer to ARD schematic for right config
PA7 is an AFIO, refer to SPI schematic for right config
U11
DP83848CVV
JP19
HR911105A
RMII MODE
MCU_+3V3
GND
Company Name: GigaDevice
File Name: ENET
Revision: 1.0
Data: 2018-10
Author: kywang

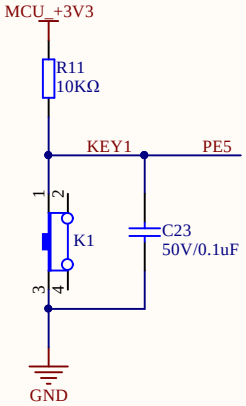


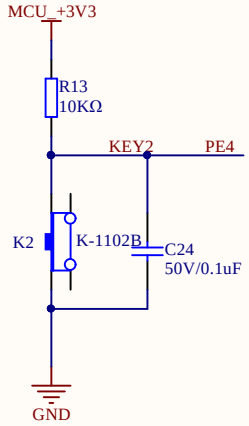


LED



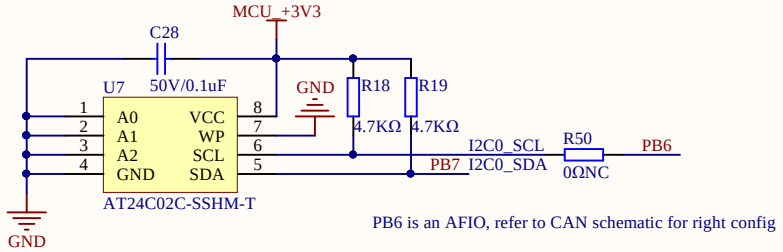
KEY





Company Name: GigaDevice		
File Name: Extension		
Revision: 1.0	Data: 2018-10	Author: kywang

I2C



Company Name: GigaDevice		
File Name: I2C_SMbus		
Revision: 1.0	Data: 2018-10	Author: kywang

U_Exmc
SDRAM.SchDoc

U_ENET
ENET.SchDoc

U_Arduino
Arduino.SchDoc

U_Power
Power.SchDoc

U_I2C
I2C.SchDoc

U_Extension
Extension.SchDoc

U_MCU
MCU.SchDoc

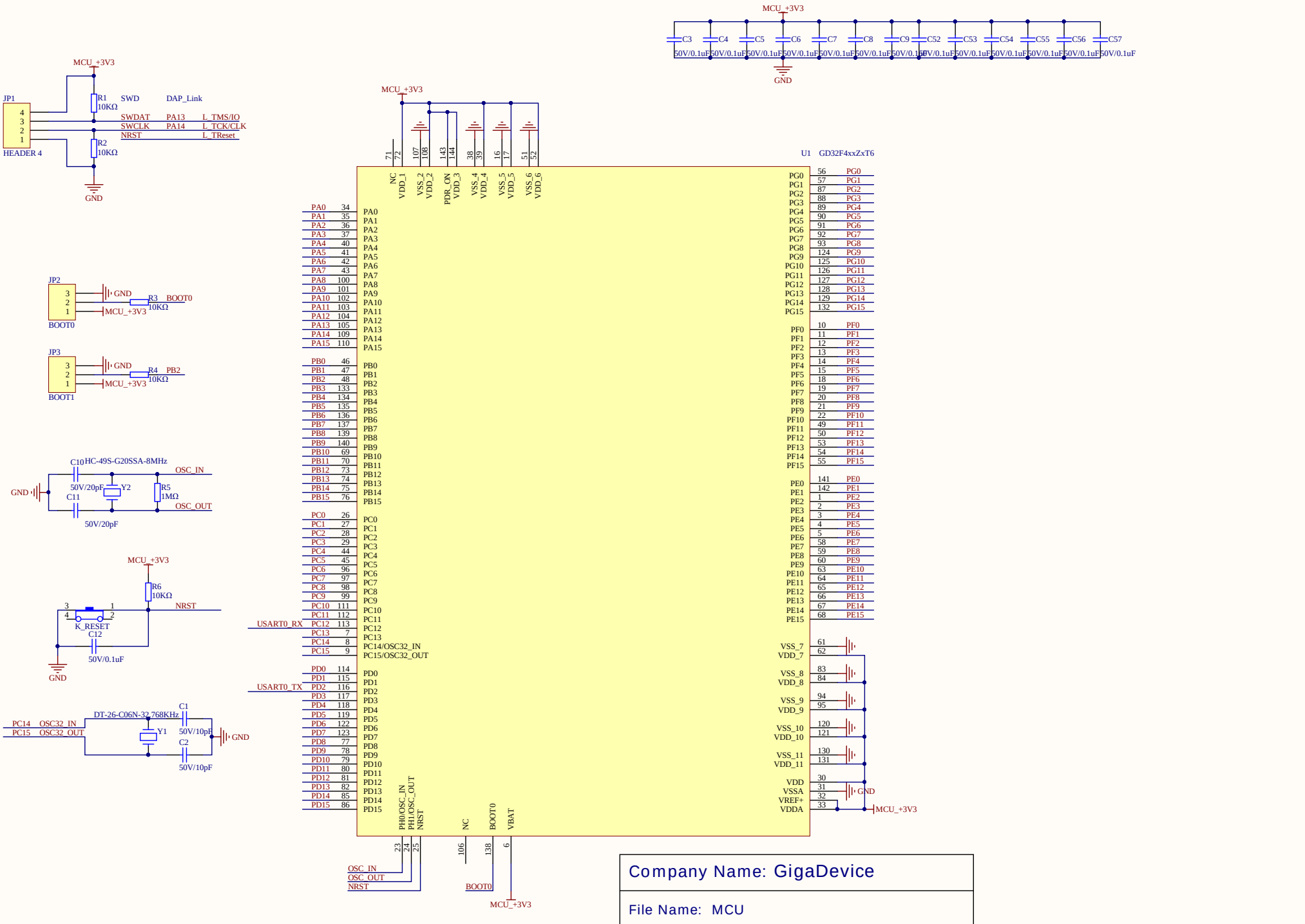
U_CAN
CAN.SchDoc

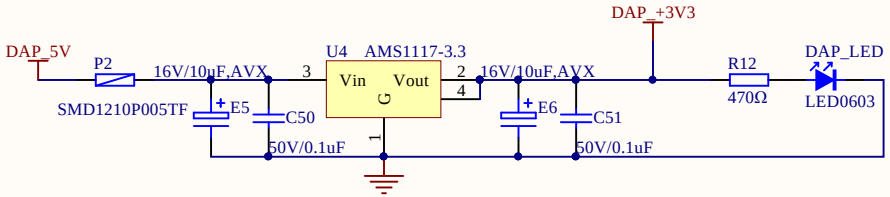
U_USB_OTG
USB_OTG.SchDoc

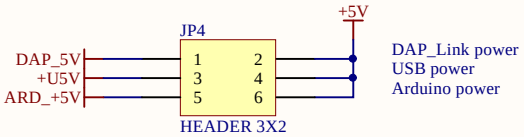
U_DAPLink
DAPLink.SchDoc

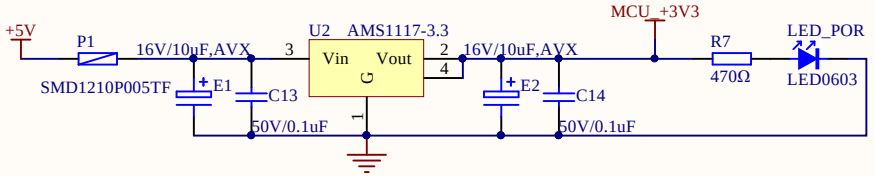
U_SPI
SPI.SchDoc

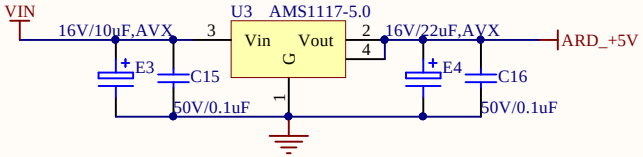
Company Name: GigaDevice		
File Name: MAIN		
Revision: 1.0	Data: 2018-10	Author: kywang


MCU_+3V3
C3 C4 C5 C6 C7 C8 C9 C52 C53 C54 C55 C56 C57
50V/0.1uF
GND
JP1
HEADER 4
R1 10KΩ
R2 10KΩ
SWD DAP_Link
SWDAT PA13 L_TMS/IO
SWCLK PA14 L_TCK/CLK
NRST L_TReset
JP2
BOOT0
R3 10KΩ BOOT0
JP3
BOOT1
R4 10KΩ PB2
C10 HC-49S-G20SSA-8MHz
50V/20pF
Y2
C11
R5 1MΩ
OSC_IN
OSC_OUT
R6 10KΩ
NRST
K_RESET
C12
50V/0.1uF
DT-26-C06N-32.768KHz
Y1
C1 C2
50V/10pF
PC14 OSC32_IN
PC15 OSC32_OUT
U1 GD32F4xxZxT6
NC VDD_1
VSS_2 VDD_2
PDR_ON VDD_3
VSS_4 VDD_4
VSS_5 VDD_5
VSS_6 VDD_6
PA0 PA1 PA2 PA3 PA4 PA5 PA6 PA7 PA8 PA9 PA10 PA11 PA12 PA13 PA14 PA15
PB0 PB1 PB2 PB3 PB4 PB5 PB6 PB7 PB8 PB9 PB10 PB11 PB12 PB13 PB14 PB15
PC0 PC1 PC2 PC3 PC4 PC5 PC6 PC7 PC8 PC9 PC10 PC11 PC12 PC13
PC14/OSC32_IN
PC15/OSC32_OUT
PD0 PD1 PD2 PD3 PD4 PD5 PD6 PD7 PD8 PD9 PD10 PD11 PD12 PD13 PD14 PD15
USART0_RX
USART0_TX
PG0 PG1 PG2 PG3 PG4 PG5 PG6 PG7 PG8 PG9 PG10 PG11 PG12 PG13 PG14 PG15
PF0 PF1 PF2 PF3 PF4 PF5 PF6 PF7 PF8 PF9 PF10 PF11 PF12 PF13 PF14 PF15
PE0 PE1 PE2 PE3 PE4 PE5 PE6 PE7 PE8 PE9 PE10 PE11 PE12 PE13 PE14 PE15
VSS_7 VDD_7
VSS_8 VDD_8
VSS_9 VDD_9
VSS_10 VDD_10
VSS_11 VDD_11
VDD VSSA VREF+ VDDA
PH0/OSC_IN
PH1/OSC_OUT
NRST
NC
BOOT0
VBAT
OSC_IN
OSC_OUT
NRST
BOOT0
MCU_+3V3
Company Name: GigaDevice
File Name: MCU
Revision: 1.0
Data: 2018-10
Author: kywang

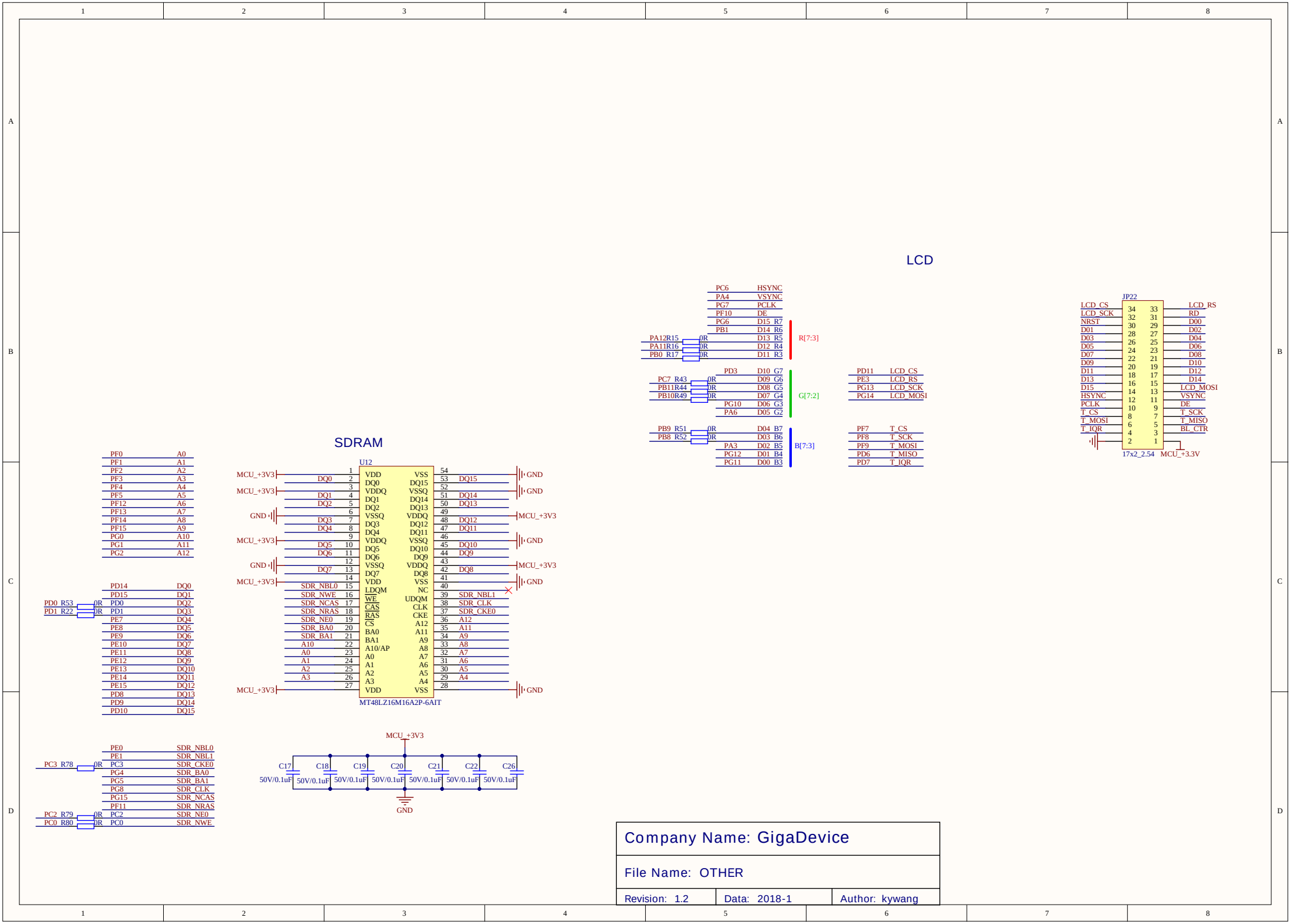


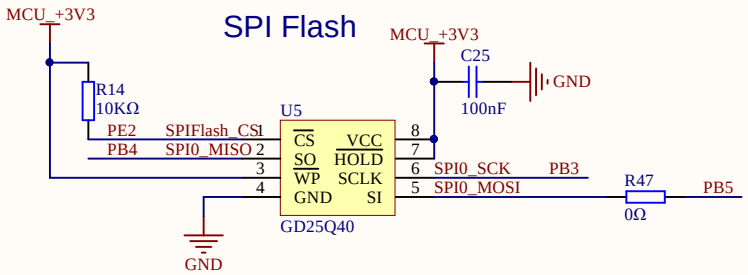




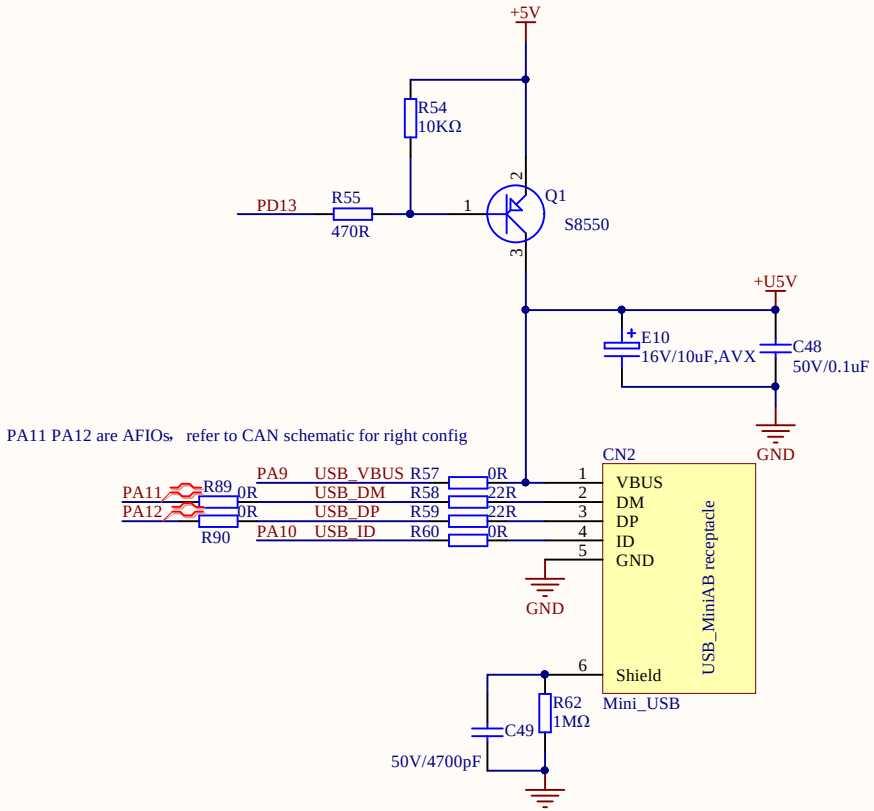


Company Name: GigaDevice		
File Name: Power		
Revision: 1.0	Data: 2018-10	Author: kywang

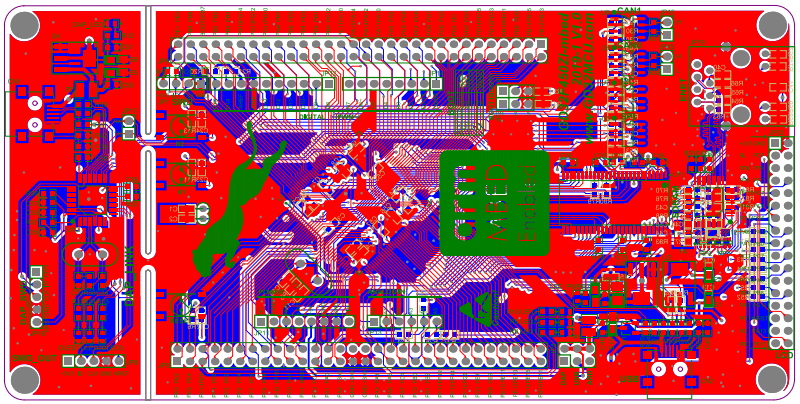

SDRAM
LCD
U12
MT48LZ16M16A2P-6AIT
JP22
17x2_2.54
R[7:3]
G[7:2]
B[7:3]
Company Name: GigaDevice
File Name: OTHER
Revision: 1.2
Data: 2018-1
Author: kywang



Company Name: GigaDevice		
File Name: SPI		
Revision: 1.0	Data: 2018-10	Author: kywang



Company Name: GigaDevice		
File Name: USBFS		
Revision: 1.1	Data: 2018-1	Author: Xufei


arm
mbed
Enabled