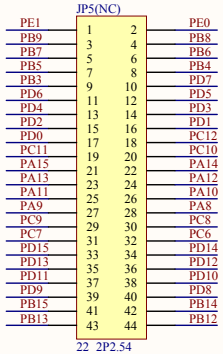
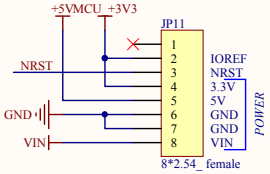
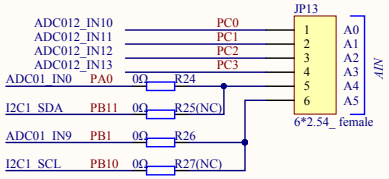
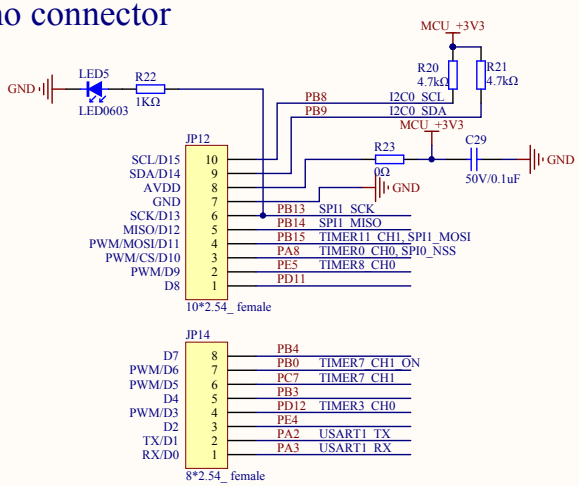
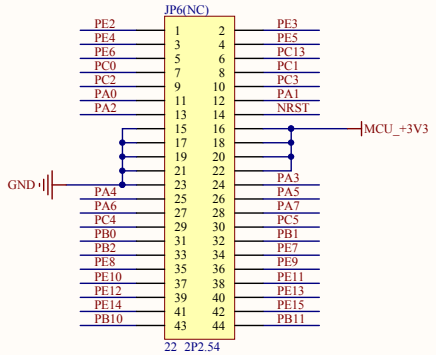
Arduino uno connector







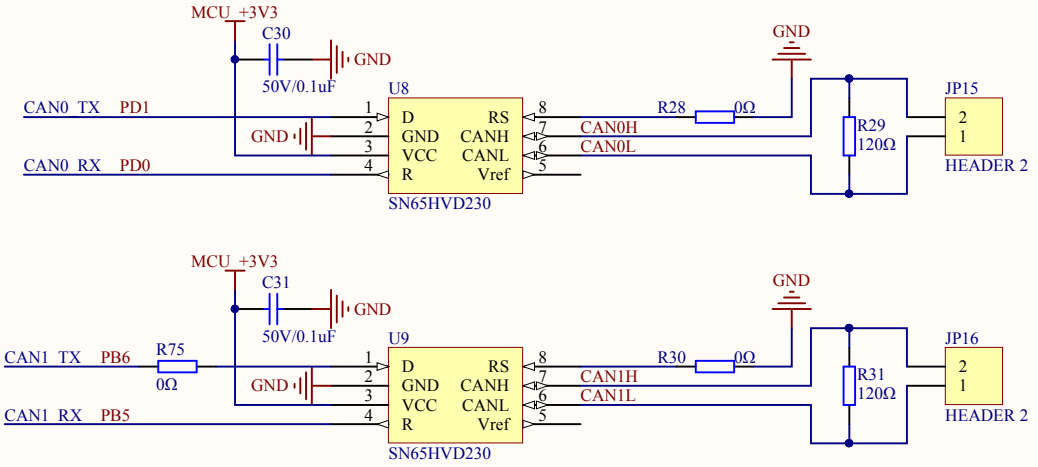




PC1 PA2 PB0 PB11 PB13 are AFIOs，refer to ENET schematic for right config

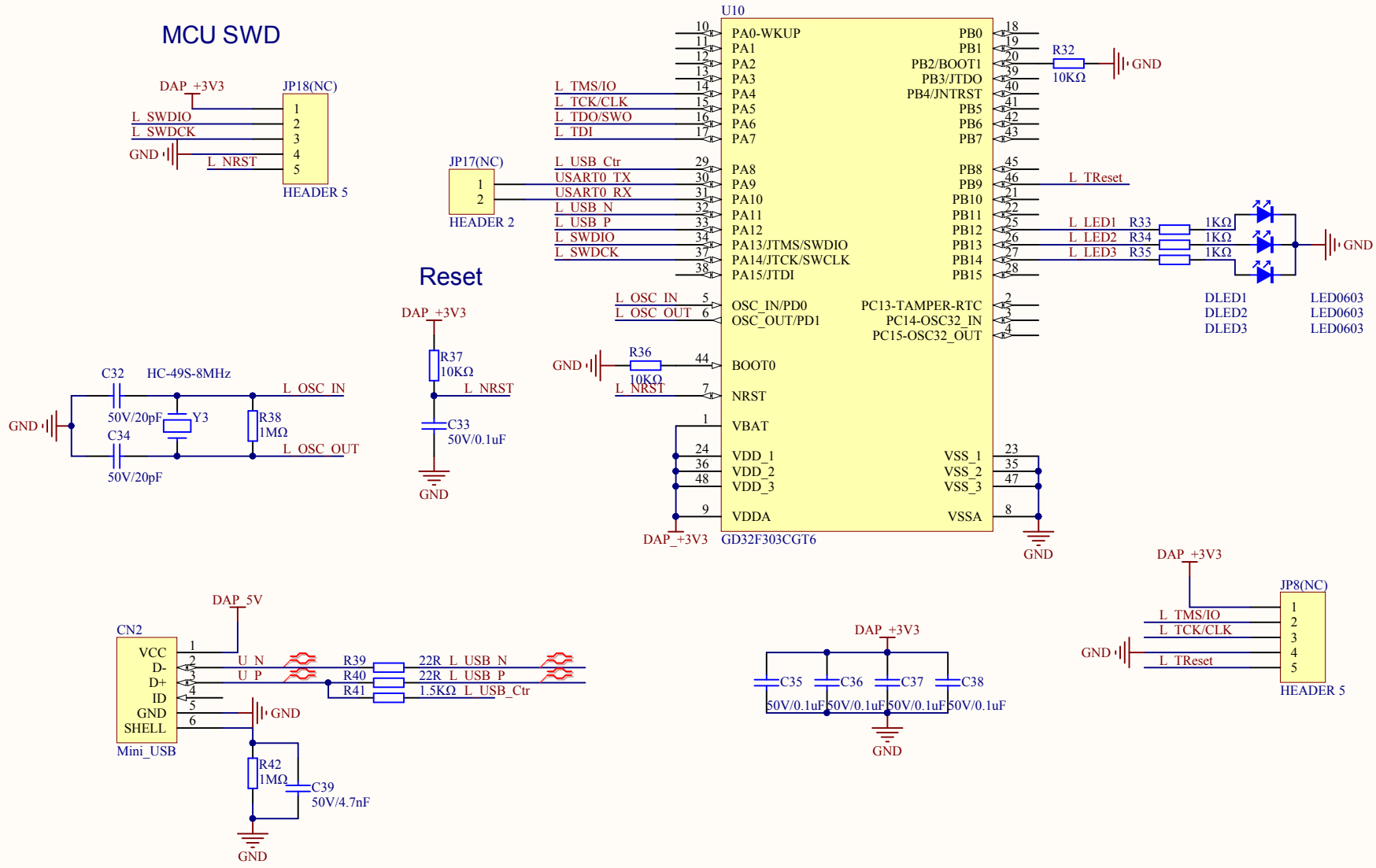
Company Name: GigaDevice		
File Name: Arduino		
Revision: 1.0	Data: 2018-10	Author: kywang

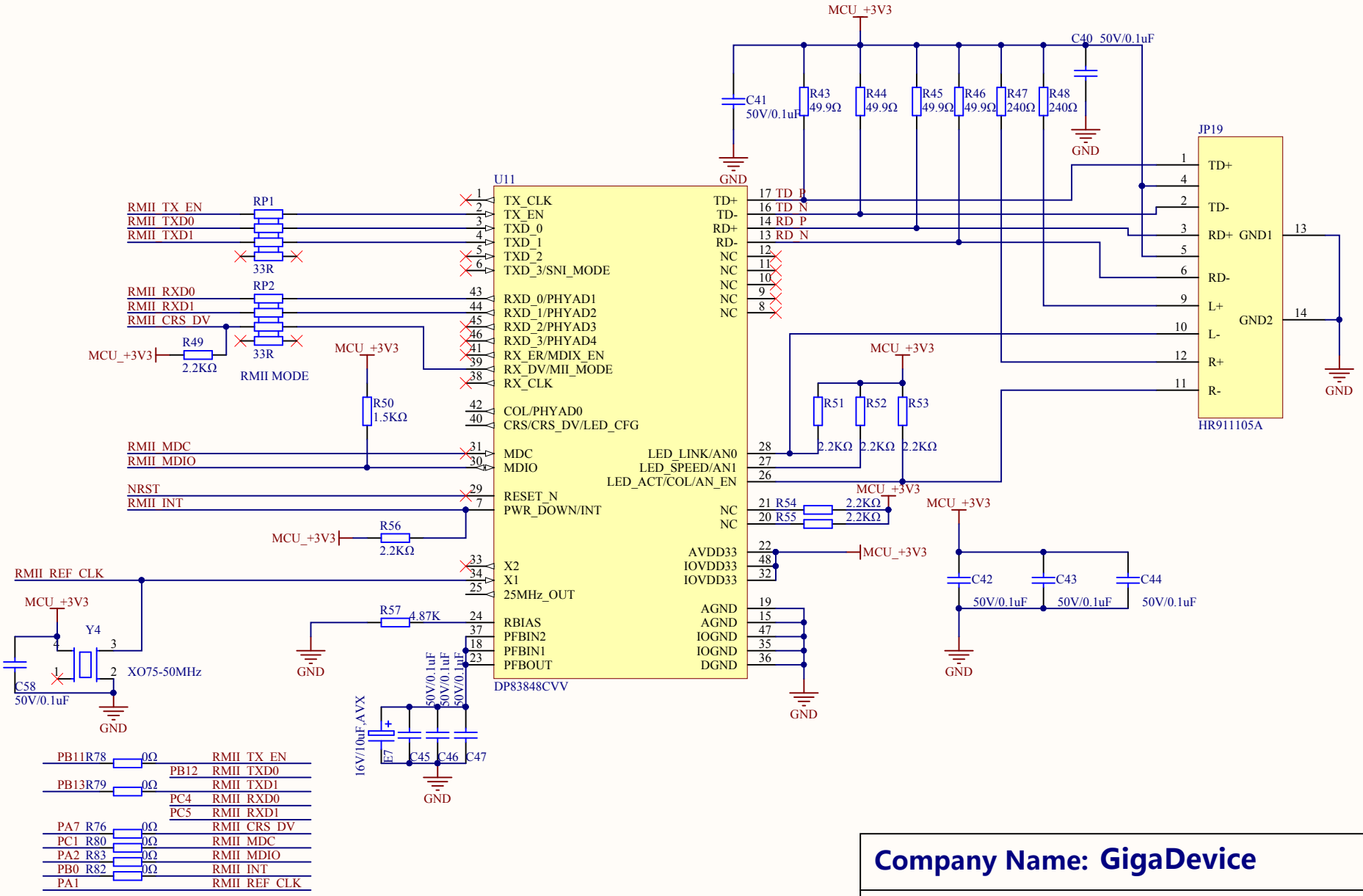
CAN

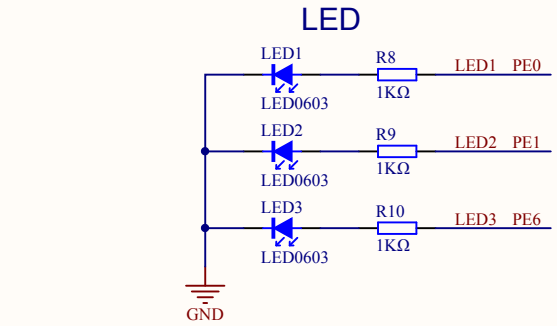


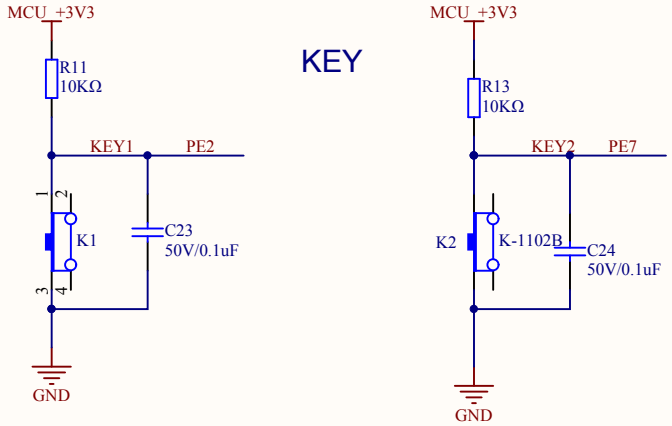
PB6 is an AFIO，refer to I2C schematic for right config

Company Name: GigaDevice		
File Name: CAN		
Revision: 1.0	Data: 2018-10	Author: kywang


MCU SWD
DAP_+3V3
JP18(NC)
L_SWDIO
L_SWDCK
GND
L_NRST
HEADER 5
U10
L_TMS/IO
L_TCK/CLK
L_TDO/SWO
L_TDI
JP17(NC)
L_USB_Ctr
USART0_TX
USART0_RX
L_USB_N
L_USB_P
L_SWDIO
L_SWDCK
HEADER 2
PA0-WKUP
PA1
PA2
PA3
PA4
PA5
PA6
PA7
PA8
PA9
PA10
PA11
PA12
PA13/JTMS/SWDIO
PA14/JTCK/SWCLK
PA15/JTDI
PB0
PB1
PB2/BOOT1
PB3/JTDO
PB4/JNTRST
PB5
PB6
PB7
PB8
PB9
PB10
PB11
PB12
PB13
PB14
PB15
R32
10KΩ
GND
L_TReset
L_LED1
L_LED2
L_LED3
R33
R34
R35
1KΩ
1KΩ
1KΩ
DLED1
DLED2
DLED3
LED0603
LED0603
LED0603
Reset
DAP_+3V3
R37
10KΩ
L_NRST
C33
50V/0.1uF
GND
L_OSC_IN
L_OSC_OUT
OSC_IN/PD0
OSC_OUT/PD1
PC13-TAMPER-RTC
PC14-OSC32_IN
PC15-OSC32_OUT
R36
10KΩ
BOOT0
NRST
VBAT
VDD_1
VDD_2
VDD_3
VDDA
VSS_1
VSS_2
VSS_3
VSSA
GD32F303CGT6
C32
HC-49S-8MHz
50V/20pF
Y3
C34
50V/20pF
R38
1MΩ
DAP_5V
CN2
VCC
D-
D+
ID
GND
SHELL
Mini_USB
U_N
U_P
R39
R40
R41
22R_L_USB_N
22R_L_USB_P
1.5KΩ_L_USB_Ctr
R42
1MΩ
C39
50V/4.7nF
C35
C36
C37
C38
50V/0.1uF
JP8(NC)
HEADER 5
Company Name: GigaDevice
File Name: DAPLink
Revision: 1.0
Data: 2018-10
Author: kywang

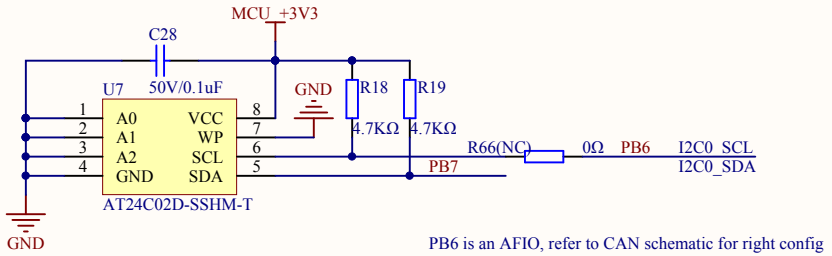

MCU_+3V3
C40 50V/0.1uF
C41 50V/0.1uF
R43 49.9Ω
R44 49.9Ω
R45 49.9Ω
R46 49.9Ω
R47 240Ω
R48 240Ω
GND
JP19
TD+
TD-
RD+ GND1
RD-
L+ GND2
L-
R+
R-
HR911105A
U11
TX_CLK
TX_EN
TXD_0
TXD_1
TXD_2
TXD_3/SNI_MODE
RXD_0/PHYAD1
RXD_1/PHYAD2
RXD_2/PHYAD3
RXD_3/PHYAD4
RX_ER/MDIX_EN
RX_DV/MII_MODE
RX_CLK
COL/PHYAD0
CRS/CRS_DV/LED_CFG
MDC
MDIO
RESET_N
PWR_DOWN/INT
X2
X1
25MHz_OUT
RBIAS
PFBIN2
PFBIN1
PFBOUT
TD+
TD-
RD+
RD-
NC
LED_LINK/AN0
LED_SPEED/AN1
LED_ACT/COL/AN_EN
AVDD33
IOVDD33
AGND
IOGND
DGND
DP83848CVV
RMII_TX_EN
RMII_TXD0
RMII_TXD1
RP1
33R
RMII_RXD0
RMII_RXD1
RMII_CRS_DV
RP2
33R
R49 2.2KΩ
RMII MODE
R50 1.5KΩ
RMII_MDC
RMII_MDIO
NRST
RMII_INT
R56 2.2KΩ
R51 2.2KΩ
R52 2.2KΩ
R53 2.2KΩ
R54 2.2KΩ
R55 2.2KΩ
RMII_REF_CLK
Y4
XO75-50MHz
C58 50V/0.1uF
R57 4.87K
C42 50V/0.1uF
C43 50V/0.1uF
C44 50V/0.1uF
E7 16V/10uF,AVX
C45 50V/0.1uF
C46 50V/0.1uF
C47 50V/0.1uF
PB11 R78 0Ω RMII_TX_EN
PB12 RMII_TXD0
PB13 R79 0Ω RMII_TXD1
PC4 RMII_RXD0
PC5 RMII_RXD1
PA7 R76 0Ω RMII_CRS_DV
PC1 R80 0Ω RMII_MDC
PA2 R83 0Ω RMII_MDIO
PB0 R82 0Ω RMII_INT
PA1 RMII_REF_CLK
PC1 PA2 PB0 PB11 PB13 are AFIOs，refer to ARD schematic for right config
PA7 is an AFIO, refer to SPI schematic for right config
Company Name: GigaDevice
File Name: ENET
Revision: 1.0
Data: 2018-10
Author: kywang





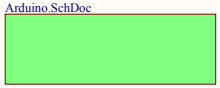
Company Name: GigaDevice		
File Name: Extension		
Revision: 1.0	Data: 2018-10	Author: kywang

I2C

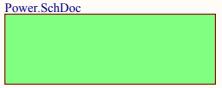


Company Name: GigaDevice		
File Name: I2C_SMbus		
Revision: 1.0	Data: 2018-10	Author: kywang

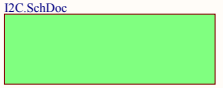
U_Arduino
Arduino.SchDoc



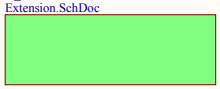
U_Power
Power.SchDoc



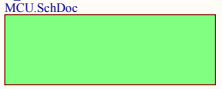
U_I2C
I2C.SchDoc



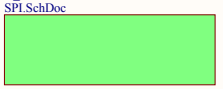
U_Extension
Extension.SchDoc



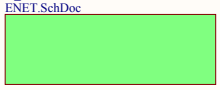
U_MCU
MCU.SchDoc



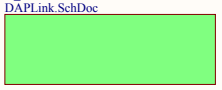
U_SPI
SPI.SchDoc



U_ENET
ENET.SchDoc

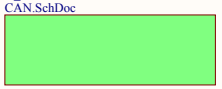


U_DAPLink
DAPLink.SchDoc

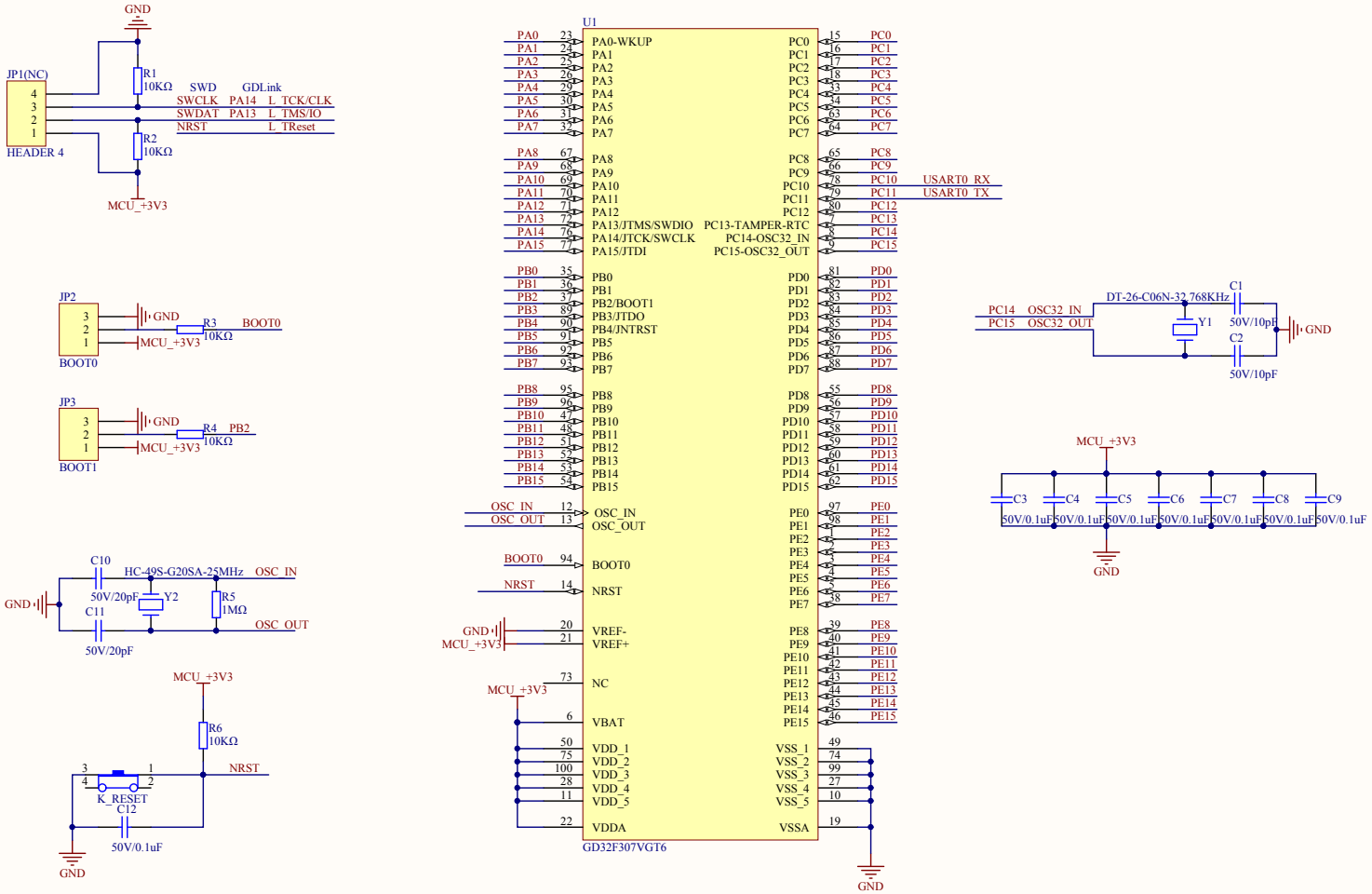


U_USB_FS
USB_FS.SchDoc

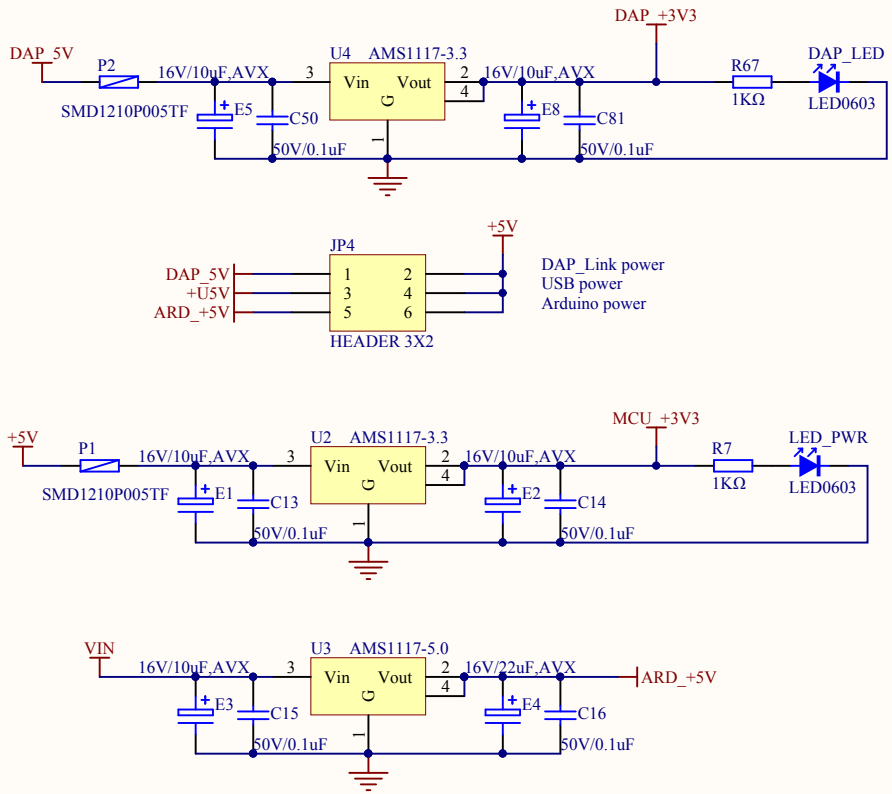
U_CAN
CAN.SchDoc



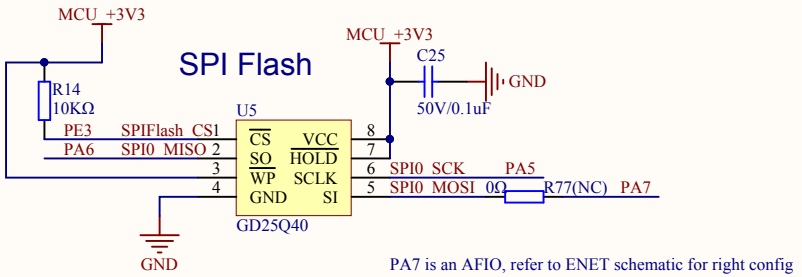
Company Name: GigaDevice		
File Name: MAIN		
Revision: 1.0	Data: 2018-10	Author: kywang



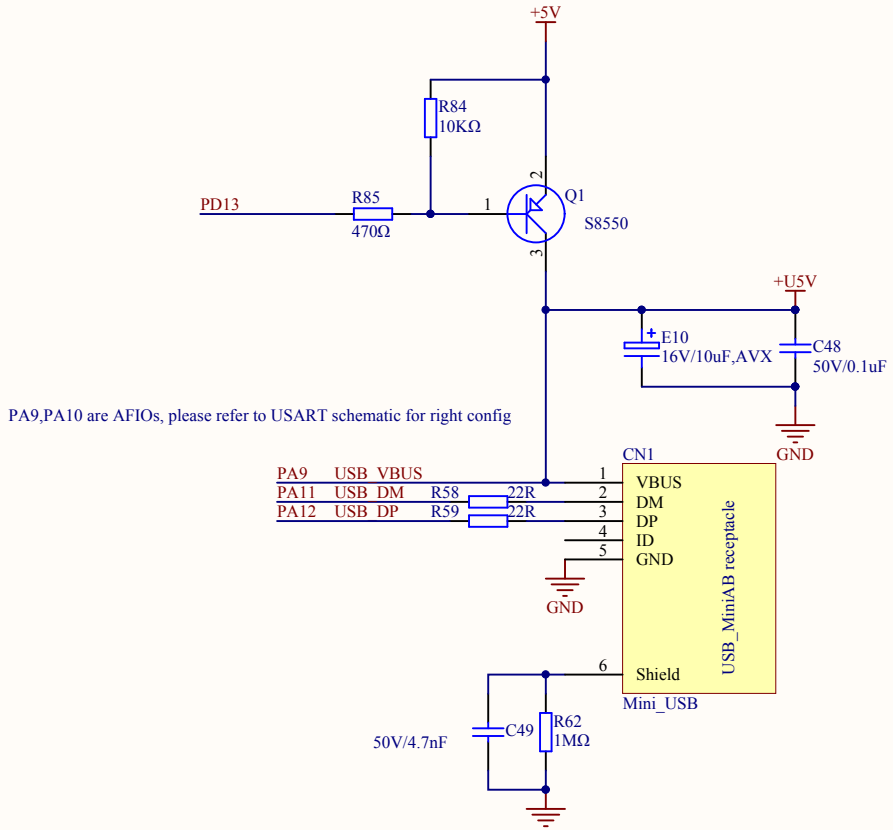
Company Name: GigaDevice		
File Name: MCU		
Revision: 1.0	Data: 2018-10	Author: kywang



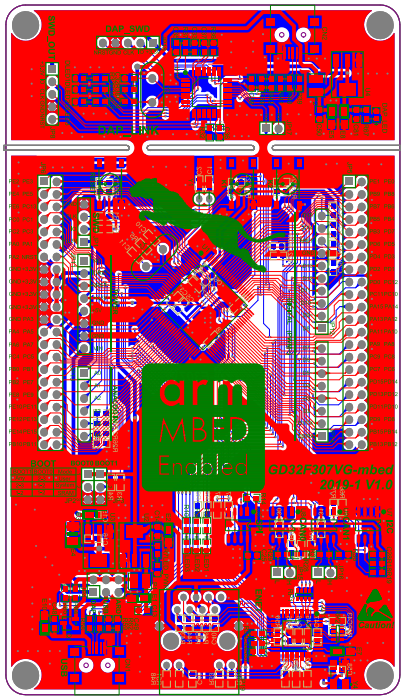
Company Name: GigaDevice		
File Name: Power		
Revision: 1.0	Data: 2018-10	Author: kywang



Company Name: GigaDevice		
File Name: SPI		
Revision: 1.0	Data: 2018-10	Author: kywang



Company Name: GigaDevice		
File Name: USBFS		
Revision: 1.1	Data: 2017-4	Author: Xufei


arm
MBED
Enabled
GD32F307VG-mbed
2019-1 V1.0